

A Cellular Logic Array for Image Processing

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Abstract—A cellular logic image processor employing 192 cells in a 16 by 12 hexagonal array is described. The processor has been constructed and its performance assessed. The various classes of functions which can be implemented in the cellular array are discussed and sample programs explained in detail.

Cellular array Pattern recognition Image processing Parallel processing Binary images

I. INTRODUCTION

A. Cellular logic arrays

MANY authors have proposed parallel processing algorithms for image processing and pattern recognition, but the state-of-the-art in circuit component technology has only recently approached the point where the construction of large arrays of interconnected cellular logic elements is an economic proposition. It would seem appropriate to process two-dimensional data sets by using two-dimensional arrays of logic elements, thus matching the form of the processor to the form of the data, so that there is an incentive to explore the action of such arrays. However, this action can be simulated on conventional serial computers and the potential power of parallel systems can be assessed by studying the simulated performance. Nevertheless, precise simulation of large, complex arrays taking into account details of hardware properties, is by no means a simple task and tends to involve lengthy programs which are both expensive to develop and expensive to run.

The research program reported in this paper is an attempt to provide general purpose programmable cellular logic arrays which can be used for image processing studies and which will facilitate the design of specialized arrays for particular image processing applications. It is suggested that the use of such arrays will usually be more satisfactory than computer simulation, both from the point of view of matching technique to problem and from consideration of processing times and efficiency.

B. Brief literature survey

As has already been implied, most studies of proposed cellular logic systems for image processing have been purely theoretical, often employing a conventional serial computer for simulation of the parallel algorithms. In 1958 UNGER⁽¹⁾ proposed and, later, simulated⁽²⁾ a square array with 36 by 36 elements and with nine memory registers in each cell. Possible instructions to each cell included left, right, up and down shift instructions, logical addition and multiplication between the cell accumulator, neighbouring cell accumulators, and the cell memories, transfers to memories, input/output, and a special "link" instruction for finding connected sets. Typical character recognition programs required of the order

Cellular Logic Image Processing

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Cellular logic image processing, 1986 **The application of cellular logic to image processing** David Michael Watson, 1974 *Morphological Image Processing: Architecture and VLSI design* P.P. Jonker, 2012-12-06 Summary Based on the experiences of past designs and the outcome of recent studies in the comparisons of low level image processing architectures a pipelined system for real time low image processing has been designed and realized in CMOS technology To minimize design pitfalls a study was performed to the details of the design solutions that have been found in embodiments of the three main architectural groups of image processing the Square Processor Arrays the Linear Processor Arrays and the Pipelines This is reflected in a theoretical model As the design is based on bitplane wise processing of images research was performed on the principles of Cellular Logic Processing of two dimensional images of binary A methodology has been developed that is based on the transformation images using sets of Hit or Miss masks This method appeared to be extendable to higher dimensional images A theoretical model for the generation of break point conditions in high dimensional images has been developed and applied up to dimension three Progress In Image Analysis And Processing - Proceedings Of The 5th International Conference Virginio Cantoni, Luigi P Cordella, Stefano Levialdi, G Sanniti Di Baja, 1990-03-01 This volume contains papers presented at the 5th International Conference on Image Analysis and Processing It covers the most important topics of current interest in the field presenting a large collection of recent results achieved by leading academic and industrial research groups from several countries It contains invited lectures and research papers dealing with theoretical and applicative aspects of Image Processing It is a valuable and updated reference source for the Image Processing community It contains advanced architectural concepts and describes new frontiers for applicants

Multicomputers and Image Processing Kendall Preston, 1982-01-01 *Multicomputers and Image Processing Algorithms and Programs* is the second of a set presenting papers from the third meeting held in Madison Wisconsin on May 27 30 1981 The workshop explores the large and powerful multicomputer arrays and networks with particular emphasis on the related aspects of developing algorithms and programs for multicomputer architectures Separating 33 papers into chapters this book reflects the three major aspects of the problem user algorithms and programs higher level languages and multicomputer architectures The first chapters present specific larger structure as well as whole program algorithms and their respective applications Other chapters describe the important high level programming of Fortran based language for the massively parallel processors and the set theory based language for expressing the structural image processing and perceptual operations effected by Cytocomputer and other cellular array motivated architectures The concluding chapters examine several major types of computer architectures that are being developed for large parallel serial multicomputer systems This book is of great value to computer programmers and scientists *A Digital Optical Cellular Image Processor* Kung-Shiuh Huang, 1990 The research detailed in this book has been motivated by the search for a simple parallel digital optical

architecture for image processing The development of a simple unified consistent theory of parallel binary image processing is described and its implementation on digital optical processors is considered Both theoretical and experimental work are included and both algorithmic and architectural designs are covered Also presented are the experimental results of the implementation of a prototype Digital Optical Cellular Image Processor DOCIP system used to demonstrate the concept of the DOCIP architecture

Mathematical Nonlinear Image Processing Edward R. Dougherty, Jaakko Astola, 2012-12-06
 Mathematical Nonlinear Image Processing deals with a fast growing research area The development of the subject springs from two factors 1 the great expansion of nonlinear methods applied to problems in imaging and vision and 2 the degree to which nonlinear approaches are both using and fostering new developments in diverse areas of mathematics Mathematical Nonlinear Image Processing will be of interest to people working in the areas of applied mathematics as well as researchers in computer vision Mathematical Nonlinear Image Processing is an edited volume of original research It has also been published as a special issue of the Journal of Mathematical Imaging and Vision Volume 2 Issue 2 3

Applications of Pattern Recognition King-Sun Fu, 2019-07-22 This monograph is intended to cover several major applications of pattern recognition After a brief introduction to pattern recognition in Chapter 1 the two major approaches statistical approach and syntactic approach are reviewed in Chapter 2 and 3 respectively Other topics include the application of pattern recognition to seismic wave interpretation to system reliability problems to medical data analysis as well as character and speech recognition

Selected Papers on Morphological Image Processing Tomasz Szoplik, 1996 Topics in this volume include grayscale morphology morphological optics statistical morphology contrast features extraction and an overview of morphological filtering

Signal Processing, Theories and Applications, 1986 *Journal of the Optical Society of America*, 1994

Nonlinear Image Processing, 1996 Parallel Architectures for Image Processing Joydeep Ghosh, Colin G. Harrison, 1990

Computer Design, 1982 *IETE Technical Review*, 1995 Mini and Microcomputers and Their Applications E. Luque, 1985

Applications of Digital Image Processing IV Andrew G. Tescher, 1983 **CAN-AM Eastern '90** Ronald L. Antos, Allen J. Krisiloff, 1991 *Computer Vision and Image Processing*, 1986 *Optical Computing* 88, 1989

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web 1 i understand that i am an independent contract driver this agreement shall remain in full force and effective for a 1 year period beginning on the date first written above and continuing thereafter on a year to year basis either party may terminate this agreement at any time with or without cause

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web i v payment amount the client agrees to pay the delivery driver the following compensation for the service performed

under this agreement check one hour per job a job is other

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