

Foreword

International Conference on Multichip Modules and High Density Packaging

THIS special section of the IEEE TRANSACTIONS ON ADVANCED PACKAGING contains papers reviewed and revised from the 1998 International Conference on Multichip Modules and High Density Packaging, Denver, CO, April 15–17, 1998. These papers cover the emerging technology areas of integrated passive components, chip-scale packaging, single and multichip flip chip packages and multifunctional structures. Five additional papers addressing manufacturing related issues in electronics packaging have been selected for publication in the IEEE TRANSACTIONS ON ELECTRONICS PACKAGING MANUFACTURING.

The field of integrated passives research and development is expanding dramatically as packaging engineers realize that passive components are requiring an increasing percentage of the substrate surface area. This is particularly true of mixed signal, wireless applications. The paper by Boltan *et al.* examines high dielectric constant materials for buried capacitors in a multilayer ceramic substrate.

Chip-scale packaging (CSP) is gaining momentum as the industry pushes the boundaries in portable electronics. Itagaki *et al.* have combined ALIVH substrate technology with gold stud bump flip-chip assembly to create a new CSP. The aramid-epoxy based substrate technology uses laser-drilled holes filled with copper paste to create high-density buried vias. Stud bumping with a gold wire bonder allows use of die designed for wire bonding without additional processing. The resulting package was shown to be reliable for portable electronics application.

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R. Wayne Johnson (F'94) received the B.E. and M.Sc. degrees from Vanderbilt University, Nashville, TN, in 1979 and 1982, respectively, and the Ph.D. degree from Auburn University, Auburn, AL, in 1987, all in electrical engineering.

He has worked in the microelectronics industry for DuPont, Eaton, and Amperex. He is an Alumni Professor of electrical engineering at Auburn University and Director of the Laboratory for Electronics Assembly and Packaging (LEAP). At Auburn, he has established teaching and research laboratories for advanced packaging and electronics assembly. Research efforts are focused on materials, processing, and modeling for multichip packages and high temperature electronics. He has worked in MCM design, MCM-L, -C, and -D substrate technology as well as SMT, wire bond and flip-chip assembly techniques. He has published and presented numerous papers at workshops and conferences and in technical journals. He has also co-edited one IEEE book on MCM technology and written two book chapters in the areas of silicon MCM technology and MCM assembly.

Dr. Johnson received the 1997 Auburn Alumni Engineering Council Senior Faculty Research Award for his work in electronics packaging and assembly, the 1993 John A. Wagnon, Jr. Technical Achievement Award from ISHM, and the Daniel C. Hughes Memorial Award in 1997. He is a member of SMTA and IPC, and was the 1991 President of the International Society for Hybrid Microelectronics (ISHM).

Jimenez *et al.* has examined the assembly and reliability of single and multichip packages. Eutectic solder flip chip and underfill was used for chip-to-package substrate assembly. Package footprints were developed to match both BGA and conventional surface mount packages such as quad flat-packs (QFP's).

In the final paper, Rawal *et al.* examine thermal management issues in multifunctional structures for spacecraft applications. Multifunctional structures integrate the electronics into the walls of the spacecraft. Thermal management is an increasingly important design issue as electronic densities continue to increase. In fact, thermal consideration may become the limiting factor in electronic density.

These papers represent approaches to addressing the challenges posed by the unremitting demand for increasingly dense electronics. For packaging technology to keep pace with the roadmap developed by the SIA, electronics packaging research must be accelerated. Communications between researchers through conferences such as the International Conference on Multichip Modules and High Density Packaging and through the IEEE TRANSACTIONS is critical.

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