

University of California
Berkeley
College of Engineering
Department of Electrical Engineering
and Computer Science

Robert W. Brodersen
EECS 40

Analog Circuit Design

Lectures
on
SINGLE TRANSISTOR CIRCUITS

Two Stages of Analysis

S-1

STEP 1: DC ANALYSIS

to Region of Operation of Transistor
to DC Currents and Voltages

STEP 2: AC ANALYSIS

to Calculate Small Signal parameters

for i_c, v_o

to Perform 2-Port analysis

$R_{in}, R_{out}, A_v, G_m$

$(A_v = G_m R_{out})$

Two Part Analysis

S-1



Small Signal
Analysis

Set independent sources to zero

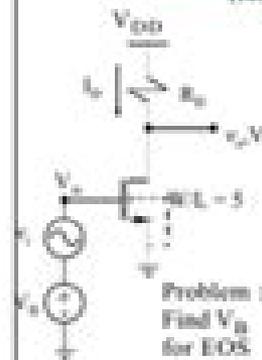
$$R_{in} = \left. \frac{v_i}{i_i} \right|_{v_o=0}$$

$$A_v = \left. \frac{v_o}{v_i} \right|_{R_L=0}$$

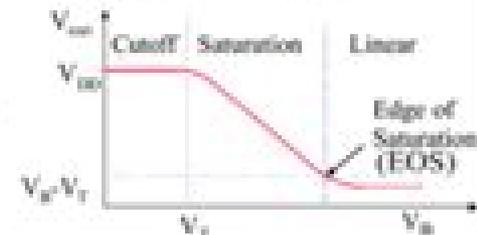
$$R_{out} = \left. \frac{v_o}{(-i_o)} \right|_{v_i=0}$$

Common Source (Inverter, Gain Stage)
(High Gain, Moderate R_{out})

S-2



$V_G = 1V$ $V_{DD} = 5V$
 $k' = 40\mu A/V^2$ $R_D = 1k\Omega$
 $\lambda = 0.01$ $\gamma = 0.1$



Problem:
Find V_B
for EOS

Analysis And Design Of Transistor Circuits

RC Schank



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