



BMW Fault Codes (DTCs)

Below is a comprehensive listing of BMW DTCs and the corresponding SAE P-codes.

Table a. Diagnostic trouble codes (DTCs)

BMW code	P-code	Fault type and function	Signal type and range	Explanation
1		Ignition coil cyl.2	Input analog timing (100 mV)	DME initiates secondary ignition for each cylinder then looks for feedback through shunt resistor in harness to determine if ignition actually occurred.
2		Ignition coil cyl.4	Input analog timing (100 mV)	DME initiates secondary ignition for each cylinder then looks for feedback through shunt resistor in harness to determine if ignition actually occurred.
3		Ignition coil cyl.6	Input analog timing (100 mV)	DME initiates secondary ignition for each cylinder then looks for feedback through shunt resistor in harness to determine if ignition actually occurred.
5	P0202	Injector circuit cyl. 2	Output digital pulse width (active low)	DME final stage will set flag whenever a short to ground, a short to battery voltage or a disconnection between output transistor and component exists.
6	P0201	Injector circuit cyl. 1	Output digital pulse width (active low)	DME final stage will set flag whenever a short to ground, a short to battery voltage or a disconnection between output transistor and component exists.
8	P0101	Mass air flow circuit range/perf.	Input analog (0-5V)	Failed signal range check against predefined diagnostic limits.
10		Engine coolant temp. circuit range/perf.	Input analog (0-5V)	Signal range is checked against predefined diagnostic limits within specific engine operations.
11		Coolant temp. coolant outlet	Input analog (0-5V)	Signal range is checked against predefined diagnostic limits within specific engine operations.
14	P0111	Intake air temp. range/performance	Input analog (0-5V)	Signal range is checked against predefined diagnostic limits within specific engine operations.
18	P1397	Exhaust cam	Input analog	Internal check of phase shift from

Bmw Abs Trouble Codes

James Taylor



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switching switches traditionally operate at layer 2 although many are capable of layer 3 routing functions as well at layer 2 they primarily work with ethernet frames and mac addresses when frames are sent through the network they are rewritten by layer 3 devices

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