

WORKSHOPS IN COMPUTING

Series edited by Professor C.J. van Rijsbergen



Geraint Jones and
Mary Sheeran (Eds.)

Designing Correct Circuits, Oxford 1990



Springer-Verlag London Ltd.

Designing Correct Circuits

J Rink



Designing Correct Circuits:

Designing Correct Circuits Geraint Jones, Mary Sheeran, 2013-12-14 These proceedings contain the papers presented at a workshop on Designing Correct Circuits jointly organised by the Universities of Oxford and Glasgow and held in Oxford on 26-28 September 1990 There is a growing interest in the application to hardware design of the techniques of software engineering As the complexity of hardware systems grows and as the cost both in money and time of making design errors becomes more apparent so there is an eagerness to build on the success of mathematical techniques in program development The harsher constraints on hardware designers mean both that there is a greater need for good abstractions and rigorous assurances of the trustworthiness of designs and also that there is greater reason to expect that these benefits can be realised The papers presented at this workshop consider the application of mathematics to hardware design at several different levels of abstraction At the lowest level of this spectrum Zhou and Hoare show how to describe and reason about synchronous switching circuits using UNILY a formalism that was developed for reasoning about parallel programs Aagaard and Leeser use standard mathematical techniques to prove correct their implementation of an algorithm for Boolean simplification The circuits generated by their formal synthesis system are thus correct by construction Thuau and Pilaud show how the declarative language LUSTRE which was designed for programming real time systems can be used to specify synchronous circuits

Special Issue on Designing Correct Circuits, 1993 *Automated Technology for Verification and Analysis* Zhiming Liu, Anders P. Ravn, 2009-09-29 This volume contains the papers presented at the 7th International Symposium on Automated Technology for Verification and Analysis held during October 13-16 in Macao SAR China The primary objective of the ATVA conferences remains the same to exchange and promote the latest advances of state of the art research on theoretical and practical aspects of automated analysis, verification and synthesis Among 74 research papers and 10 tool papers submitted to ATVA 2009 the Program Committee accepted 23 as regular papers and 3 as tool papers In all 33 experts from 17 countries worked hard to make sure that every submission received a rigorous and fair evaluation In addition the program included three excellent tutorials and keynote talks by Mark Greenstreet U British Columbia Orna Grumberg Technion and Bill Roscoe Oxford University The conference organizers were truly grateful to have such distinguished researchers as keynote speakers Many worked hard and offered their valuable time so generously to make ATVA 2009 successful First of all the conference organizers thank all 229 researchers who worked hard to complete and submit papers to the conference The PC members, reviewers and Steering Committee members also deserve special recognition Without them a competitive and peer reviewed international symposium simply cannot take place Many organizations sponsored the symposium They include The United Nations University International Institute of Software Technology UNU IIST Macao Polytechnic Institute MPI Macao POST and Formal Methods Europe FME The conference organizers thank them for their generous support and assistance

Designing Correct Circuits Jørgen Staunstrup, 1993 **Designing Correct Circuits**

Mary Sheeran,1996 The main theme of the 3rd Workshop on Designing Correct Circuits DCC96 held in BA stad Sweden 2 4 September 1996 was the discussion of the very latest advances in hardware verification and correct circuit design techniques Among the main topics covered are improving hardware designs by simplifying their proof an integrated approach to verifying large circuits deriving handshake modules for a multi target hardware compiler and serialising heterogeneous and non factorisable processor arrays The full papers from Designing Correct Circuits are available on line at <http://www.springer.co.uk/eWiC/Workshops/DCC96.html> This booklet provides the passwords needed to access the web site Purchasing the booklet provides the user with unlimited access to the site for four years subject of course to entry of the correct passwords each time

Designing Pneumatic Control Circuits Bruce E. McCord,1983 *A Circuit Grammar for Operational Amplifier Design* Andrew Lewis Ressler,1984 Electrical circuit designers seldom create really new topologies or use old ones in a novel way Most designs are known combinations of common configurations tailored for the particular problem at hand In this thesis I show that much of the behavior of a designer engaged in such ordinary design can be modeled by a clearly defined computational mechanism executing a set of stylized rules Each of my rules embodies a particular piece of the designer s knowledge A circuit is represented as a hierarchy of abstract objects each of which is composed of other objects The leaves of this tree represent the physical devices from which physical circuits are fabricated By analogy with context free languages a class of circuits is generated by a phrase structure grammar of which each rule describes how one type of abstract object can be expanded into a combination of more concrete parts Circuits are designed by first postulating an abstract object which meets the particular design requirements This object is then expanded into a concrete circuit by successive refinement using rules of my grammar There are in general many rules which can be used to expand a given abstract component Analysis must be done at each level of the expansion to constrain the search to a reasonable set

The Kidwell Two-flow Ring-circuit Water Tube Boiler Edgar Kidwell,1923 **VLSI Design Techniques for Analog and Digital Circuits** Randall L. Geiger,Phillip E. Allen,Noel R. Strader,1990 **Telephone Engineer** ,1918 **Pacific Marine Review** ,1924 **The Journal of the Institution of Electrical Engineers** ,1929 *Electricity in the Service of Man* R. Mullineux Walmsley,1921 *Power and the Engineer* ,1909 **Electrical Installation Record** ,1923 **Electrical Record and Buyer's Reference** ,1919 **The British National Bibliography** Arthur James Wells,1998 **Railway Signaling and Communications** ,1932 *Mathematics of Program Construction* ,1992 [International Conference on Computer Aided Design and Manufacture of Electronic Components, Circuits, and Systems, 3-6 July 1979, University of Sussex](#) ,1979

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