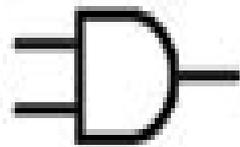
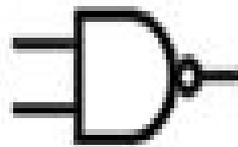


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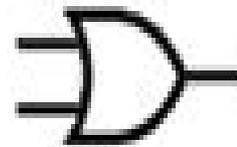
INPUT		OUTPUT
A	B	
0	0	0
1	0	0
0	1	0
1	1	1



AND



NAND



OR



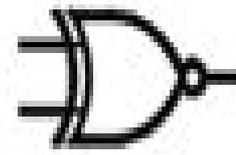
NOR



NOT



XOR



XNOR



A AND B	$A \cdot B$
A OR B	$A + B$
NOT A	\overline{A}
A XOR B	$A \oplus B$

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Jiyuan Zhang



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Table of Contents Basic Digital Electronics

1. Understanding the eBook Basic Digital Electronics
 - The Rise of Digital Reading Basic Digital Electronics
 - Advantages of eBooks Over Traditional Books
2. Identifying Basic Digital Electronics
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Basic Digital Electronics
 - User-Friendly Interface
4. Exploring eBook Recommendations from Basic Digital Electronics
 - Personalized Recommendations
 - Basic Digital Electronics User Reviews and Ratings
 - Basic Digital Electronics and Bestseller Lists
5. Accessing Basic Digital Electronics Free and Paid eBooks
 - Basic Digital Electronics Public Domain eBooks

- Basic Digital Electronics eBook Subscription Services
- Basic Digital Electronics Budget-Friendly Options
- 6. Navigating Basic Digital Electronics eBook Formats
 - ePub, PDF, MOBI, and More
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 - Basic Digital Electronics Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Basic Digital Electronics
 - Highlighting and Note-Taking Basic Digital Electronics
 - Interactive Elements Basic Digital Electronics
- 8. Staying Engaged with Basic Digital Electronics
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Basic Digital Electronics
- 9. Balancing eBooks and Physical Books Basic Digital Electronics
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Basic Digital Electronics
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Basic Digital Electronics
 - Setting Reading Goals Basic Digital Electronics
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Basic Digital Electronics
 - Fact-Checking eBook Content of Basic Digital Electronics
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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