

Computer Architecture

A Quantitative Approach

计算机体系结构 量化研究方法

(英文版·第2版)

(美) David A. Patterson 著
John L. Hennessy 著

Computer Architecture A Quantitative Approach Revised 2nd Ed

Ovid Wallace Eshbach



Computer Architecture A Quantitative Approach Revised 2nd Ed:

Computer Architecture John L. Hennessy, David A. Patterson, 2017-11-23 Computer Architecture A Quantitative Approach Sixth Edition has been considered essential reading by instructors students and practitioners of computer design for over 20 years The sixth edition of this classic textbook from Hennessy and Patterson winners of the 2017 ACM A M Turing Award recognizing contributions of lasting and major technical importance to the computing field is fully revised with the latest developments in processor and system architecture The text now features examples from the RISC V RISC Five instruction set architecture a modern RISC instruction set developed and designed to be a free and openly adoptable standard It also includes a new chapter on domain specific architectures and an updated chapter on warehouse scale computing that features the first public information on Google s newest WSC True to its original mission of demystifying computer architecture this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening while always keeping an emphasis on good engineering design Winner of a 2019 Textbook Excellence Award Texty from the Textbook and Academic Authors Association Includes a new chapter on domain specific architectures explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore s Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse scale computing with the first public information on the newest Google WSC Offers updates to other chapters including new material dealing with the use of stacked DRAM data on the performance of new NVIDIA Pascal GPU vs new AVX 512 Intel Skylake CPU and extensive additions to content covering multicore architecture and organization Includes Putting It All Together sections near the end of every chapter providing real world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available online Includes updated and improved case studies and exercises ACM named John L Hennessy and David A Patterson recipients of the 2017 ACM A M Turing Award for pioneering a systematic quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry

Applied Combinatorics, Third Edition Fred S. Roberts, Barry Tesman, 2024-06-03 The original goal of writing this book was to introduce the reader to the tools of combinatorics from an applied point of view This third edition of Applied Combinatorics was substantially rewritten There are many new examples and exercises References throughout the book to modern literature and real applications a key feature of the book have been updated and expanded The exposition continues to be updated with each new edition as the first edition was published 40 years ago The emphasis on applications from computer science genetics experimental design chemistry scheduling voting and other topics remains a central feature of the book Unique to the literature is that entire sections focus on applications such as switching functions the use of enzymes to uncover unknown RNA chains searching and sorting problems of information retrieval construction of error correcting codes

counting of chemical compounds calculation of power in voting situations and uses of Fibonacci numbers There are entire sections on applications of recurrences involving convolutions applications of eulerian chains and applications of generating functions The book continues to be based on the authors philosophy that the best way to learn mathematics is through problem solving Combinatorics can be a wonderful mechanism for introducing students to proofs However the book is not designed for an introduction to proofs course The authors treat proofs as rather informal and many of the harder proofs in the book are optional Applied Combinatorics Third Edition is divided into four parts The first part introduces the basic tools of combinatorics and their applications The remaining three parts are organized around the three basic problems of combinatorics the counting problem the existence problem and the optimization problem Most of the book is written for a first course on the topic at the undergraduate level On the other hand at a fast pace there is more than enough material for a challenging graduate course This book first appeared when courses on combinatorics were rare We are pleased to think that through its use the book has helped to establish a key course in many colleges and universities throughout the world We hope that this new edition will remain a valuable tool for instructors and students alike

COMPUTER ORGANIZATION AND DESIGN, THIRD EDITION CHAUDHURI, P. PAL, 2008-04-15 The merging of computer and communication technologies with consumer electronics has opened up new vistas for a wide variety of designs of computing systems for diverse application areas This revised and updated third edition on Computer Organization and Design strives to make the students keep pace with the changes both in technology and pedagogy in the fast growing discipline of computer science and engineering The basic principles of how the intended behaviour of complex functions can be realized with the interconnected network of digital blocks are explained in an easy to understand style

WHAT IS NEW TO THIS EDITION Includes a new chapter on Computer Networking Internet and Wireless Networks Introduces topics such as wireless input output devices RAID technology built around disk arrays USB SCSI etc Key Features Provides a large number of design problems and their solutions in each chapter Presents state of the art memory technology which includes EEPROM and Flash Memory apart from Main Storage Cache Virtual Memory Associative Memory Magnetic Bubble and Charged Couple Device Shows how the basic data types and data structures are supported in hardware Besides students practising engineers should find reading this design oriented text both useful and rewarding

Computer Architecture John L. Hennessy, David A. Patterson, Christos Kozyrakis, 2025-12-02 Computer Architecture A Quantitative Approach has been considered essential reading by instructors students and practitioners of computer design for nearly 30 years The seventh edition of this classic textbook from John Hennessy and David Patterson w Winner of a 2019 Textbook Excellence Award Texty from the Textbook and Academic Authors Association Each chapter follows a consistent framework explanation of the ideas in each chapter a crosscutting issues section which presents how the concepts covered in one chapter connect with those given in other chapters a putting it all together section that links these concepts by discussing how they are applied in real machine and

detailed examples of misunderstandings and architectural traps commonly encountered by developers and architects Includes Putting It All Together sections near the end of every chapter providing real world technology examples that demonstrate the principles covered in each chapter Covers new developments in GPU and CPU architectures as well as domain specific architectures Features more comprehensive coverage of systems on chip and heterogeneity

Professional Multicore Programming Cameron Hughes, Tracey Hughes, 2011-03-23 Professional Multicore Programming Design and Implementation for C Developers presents the basics of multicore programming in a simple easy to understand manner so that you can easily apply the concepts to your everyday projects Learn the fundamentals of programming for multiprocessor and multithreaded architecture progress to multi core programming and eventually become comfortable with programming techniques that otherwise can be difficult to understand Anticipate the pitfalls and traps of concurrency programming and synchronization before you encounter them yourself by finding them outlined in this indispensable guide to multicore programming

Handbook of Networked and Embedded Control Systems Dimitrios Hristu-Varsakelis, William S. Levine, 2007-11-14 The vast majority of control systems built today are embedded that is they rely on built in special purpose digital computers to close their feedback loops Embedded systems are common in aircraft factories chemical processing plants and even in cars a single high end automobile may contain over eighty different computers The design of embedded controllers and of the intricate automated communication networks that support them raises many new questions practical as well as theoretical about network protocols compatibility of operating systems and ways to maximize the effectiveness of the embedded hardware This handbook the first of its kind provides engineers computer scientists mathematicians and students a broad comprehensive source of information and technology to address many questions and aspects of embedded and networked control Separated into six main sections Fundamentals Hardware Software Theory Networking and Applications this work unifies into a single reference many scattered articles websites and specification sheets Also included are case studies experiments and examples that give a multifaceted view of the subject encompassing computation and communication considerations

Memory Systems Bruce Jacob, David Wang, Spencer Ng, 2010-07-28 Is your memory hierarchy stopping your microprocessor from performing at the high level it should be Memory Systems Cache DRAM Disk shows you how to resolve this problem The book tells you everything you need to know about the logical design and operation physical design and operation performance characteristics and resulting design trade offs and the energy consumption of modern memory hierarchies You learn how to tackle the challenging optimization problems that result from the side effects that can appear at any point in the entire hierarchy As a result you will be able to design and emulate the entire memory hierarchy Understand all levels of the system hierarchy Xcache DRAM and disk Evaluate the system level effects of all design choices Model performance and energy consumption for each component in the memory hierarchy

The Compiler Design Handbook Y.N. Srikant, Priti Shankar, 2018-10-03 Today s embedded devices and

sensor networks are becoming more and more sophisticated requiring more efficient and highly flexible compilers Engineers are discovering that many of the compilers in use today are ill suited to meet the demands of more advanced computer architectures Updated to include the latest techniques The Compiler Design Handbook Second Edition offers a unique opportunity for designers and researchers to update their knowledge refine their skills and prepare for emerging innovations The completely revised handbook includes 14 new chapters addressing topics such as worst case execution time estimation garbage collection and energy aware compilation The editors take special care to consider the growing proliferation of embedded devices as well as the need for efficient techniques to debug faulty code New contributors provide additional insight to chapters on register allocation software pipelining instruction scheduling and type systems Written by top researchers and designers from around the world The Compiler Design Handbook Second Edition gives designers the opportunity to incorporate and develop innovative techniques for optimization and code generation

Artificial Intelligence in Internet of Things (IoT): Key Digital Trends Frank Lin,David Pastor,Nishtha Kesswani,Ashok Patel,Sushanta Bordoloi,Chaitali Koley,2024-10-16 This book is a collection of high quality research papers presented at 8th International Conference on Internet of Things and Connected Technologies ICIoTCT 2023 held at National Institute of Technology NIT Mizoram India during 29 30 September 2023 This book presents recent advances on IoT and connected technologies This book is designed for marketing managers business professionals researchers academicians and graduate level students seeking to learn how IoT and connecting technologies increase the amount of data gained through devices enhance customer experience and widen the scope of IoT analytics in enhancing customer marketing outcomes

The Architecture of Computer Hardware and Systems Software Irv Englander,2000-02-14 This newly revised reference presents fundamental computer hardware systems software and data concepts It provides a careful in depth non engineering introduction to the inner workings of modern computer systems The book also features the latest advances in operating system design and computer interconnection

Eshbach's Handbook of Engineering Fundamentals Ovid Wallace Eshbach,2009-01-27 With specialization now the norm in engineering students preparing for the FE and PE exams and practitioners going outside their specialty need a general reference with material across a number of disciplines Since 1936 Eshbach s Handbook of Engineering Fundamentals has been the bestselling reference covering the general principles of engineering today it s more relevant than ever For this Fifth Edition respected author Myer Kutz fully updates and reshapes the text focusing on the basics the important formulas tables and standards necessary for complete and accurate knowledge across engineering disciplines With chapters on mathematical principles physical units and standards as well as the fundamentals of mechanical aerospace electrical chemical and industrial engineering this classic reference is more relevant than ever to both practicing engineers and students studying for the FE and PE exams

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Advances in Systems, Signals, Control and Computers Vladimir B.. Bajić,1998

Proceedings of the Sixteenth ACM Symposium on Operating Systems Principles, 1997 American Book Publishing Record, 2006 IEICE Transactions on Electronics, 2002 **Annual ACM Symposium on Parallel Algorithms and Architectures**, 2000 Network Processors Panos C. Lekkas, 2003-07-28 Table of contents **Report**, 2000 **ACM Transactions on Computer Systems**, 2002

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