

Mark Scheme

The 11+ Creative Writing Paper is not included in all 11+ examination papers and is not a requirement set by all grammar schools. When it is included as part of the 11+ examinations, the way in which the paper is marked can differ between examination boards and consortiums.

The mark scheme provided here suggests ideas for tutors to use when marking the work completed by students on the 11+ Creative Writing Baseline Assessment Papers. Tutors can use these ideas as an initial starting point but should also refer to guidance set by the examination board that is relevant to each of their 11+ students' chosen grammar school(s).

Key Writing Area	Required Skills
Meeting the required purpose of the task	Write in the required style, i.e. written a description instead of a story or written a journalistic piece
	Use paragraphs to structure the work and create impact
	Use all five senses within the descriptive language utilised
	Use a captivating opening sentence
	Ensure there is a clear ending
Using appropriate and engaging language	Write with a wide range of interesting vocabulary, using a range of verbs, adverbs and adjectives
	Keep verbs in the correct tense throughout
	Include metaphors and similes where appropriate
	Use language to create a successful tone, atmosphere and mood
Using a sophisticated writing style	Use a good mixture of simple, compound and complex sentences
	Vary the opening words in sentences and paragraphs (e.g. avoid the use of "I" to start each new sentence)
	Create characters (when appropriate) that are consistent throughout
Correctly applying spelling, grammar and punctuation rules	Ensure all words are spelt correctly
	Use a range of different punctuations
	Ensure that all sentences make sense and that there is flow from one sentence to the next

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web basic logic gates esd chapter 2 figure 2 3 every vhdl design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

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web quartus and vhdl code in this section you should learn how to implement a vhdl code for single cycle data path before we go any further let s define some of the terms that we use throughout the book entity all designs are expressed in terms of entities an entity is the most basic building block in a design

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