

whether the NPX has detected one of six possible exception conditions since those bits were last cleared or reset. They are "sticky" bits, and can only be cleared by instructions FINIT, FCLEX, FLDENV, FSAVE, and FRSTOR.

Bit 6 is the stack fault (SF) bit. This bit distinguishes invalid operations due to overflow or underflow from other kinds of invalid operations. When SF is set, bit 7 distinguishes between stack overflow ( $C_1 = 1$ ) and underflow ( $C_1 = 0$ ).

### 2.1.3 Control Word

The NPX provides the programmer with several processing options, which are selected by loading a word from memory into the control word. Figure 2-3 shows the format and encoding of the fields in the control word.

The low-order byte of this control word configures the 387 DX exception masking. Bits 0-5 of the control word contain individual masks for each of the six exception conditions recognized by the 387 DX. The high-order byte of the control word configures the DX processing options, including

- Precision control
- Rounding control

The precision-control bits (bits 8-9) can be used to set the 387 DX internal operation precision at less than the default precision (64-bit significand). These control bits can be used to provide compatibility with the earlier-generation arithmetic processors having less precision than the 387 DX. The precision-control bits affect the results of only the following five arithmetic instructions: ADD, SUB(R), MUL, DIV(R), and SQRT. All other operations are affected by PC.

The rounding-control bits (bits 10-11) provide for the common round-to-nearest mode, as well as directed rounding and true chop. Rounding control affects only the arithmetic instructions (refer to Chapter 3 for lists of arithmetic and nonarithmetic instructions).

### 2.1.4 The NPX Tag Word

The tag word indicates the contents of each register in the register stack, as shown in Figure 2-4. The tag word is used by the NPX itself to distinguish between empty and nonempty register locations. Programmers of exception handlers may use this tag information to check the contents of a numeric register without performing complex decoding of the actual data in the register. The tag values from the tag word correspond to physical registers 0-7. Programmers must use the current top-of-stack (TOP) pointer stored in the NPX status word to associate these tag values with the relative stack registers ST(0) through ST(7).

The exact values of the tags are generated during execution of the FSTENV and FSAVE instructions according to the actual contents of the nonempty stack locations. During execution of other instructions, the 387 DX updates the TW only to indicate whether a stack location is empty or nonempty.

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**Brendan G. Carr**



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