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Digitale Logik

Heinrich Bezner

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treatment of fields such as Mechanical Engineering Transportation Production Engineering Electrical Engineering Chemistry Physics Electricity Construction Food Technology Railway Engineering Automotive Engineering and more Der deutsch englische Band dieses renommierten W rterbuchs deckt rund 60 Fachgebiete ab darunter eine vertiefte Behandlung von Fachgebieten wie Maschinenbau Transportwesen Produktionstechnik Elektrotechnik Chemie Physik Elektrizit t Bauwesen Lebensmitteltechnik Eisenbahntechnik Fahrzeugtechnik und mehr **Nuclear Science Abstracts** ,1970 **Dictionary of information technology** Hans-Dieter Junge,1990 This dictionary lists more than 50 000 English and American entries from information technology with numerous references to special fields additional explanations phraseological references synonyms examples for different usage and word combinations Included are acquisition measurement and sensor technology conversion coding transmission processing EDP computer technology storing retrieval representation and utilization e g control engineering robotics Al of information as well as related terms of fields as logics reliability engineering and statistics Also treated are terms describing components devices plants methods and techniques **Fachwörterbuch Industrielle Elektrotechnik, Energie- und Automatisierungstechnik/Dictionary of Electrical Engineering, Power Engineering and Automation, Teil 2: Englisch-Deutsch / Part 2: English-German** Heinrich Bezner,1998 This technical dictionary covers the field of power engineering electrical installation and the rapidly expanding field of automation The subjects are covered by approx 70 000 entries in Part 1 and 52 000 entries in Part 2 The 4th edition covers additionally the field of fiber optic systems process control engineering and standardized bus systems used in automation PROFIBUS electrical installation EIB and Power engineering of Low voltage devices AS Interface Sources referred to for this dictionary include VDE and VDI specifications EN IEC BS ANSI CEE and ISO standards technical literature and descriptions and operating instructions from German British and American Companies The result of the European harmonization of electrotechnical regulations DIN VDE EN IEC CEE have been taken into account **Dictionary of Electrical Engineering and Electronics** Peter-Klaus Budig,1985 Dictionary of Microprocessor Systems René Schulz,Jurij Vladimirovič Chapancev,1984 **Subject Catalog** Library of Congress,1978 **Library of Congress Catalogs** Library of Congress,1976 **Digital Information Processors** Walter Hoffmann,1962 **Digital Twins in der Manufacturing Industry. Systematische Entwicklung eines Digital Twin Demonstrators** Boran Bogdanow,2021-06-08 Masterarbeit aus dem Jahr 2020 im Fachbereich Informatik Wirtschaftsinformatik Note 1 7 Technische Universit t Chemnitz Wirtschaftswissenschaften Sprache Deutsch Abstract 1 EINLEITUNG 1 1 MOTIVATION 1 2 ZIELSTELLUNG UND FORSCHUNGSFRAGE 1 3 METHODIK UND VORGEHEN 2 GRUNDLAGEN 2 1 DIGITAL TWINS 2 1 1 Evolution und Konzept von Digital Twins 2 1 2 Architektur von Digital Twins 2 1 3 Kommunikation von Digital Twins 2 1 4 Sensoren und Aktuatoren 2 2 DEMONSTRATOR 2 2 1 Modell 2 2 2 Simulation 2 2 3 Prototyping 2 2 4 Zusammenfassung der Erkenntnisse 2 3 VORGEHENSMODELL 2 3 1 Allgemeine Einordnung und Abgrenzung 2 3 2 Relevante Arten von Vorgehensmodellen 3 ENTWICKLUNG DES ARTEFAKTS 3 1

ANALYSE EXISTIERENDER ANS TZE 3 2 ABLEITUNG VON ANFORDERUNGEN AN DAS VORGEHENSMODELL 3 3 SYSTEMATISCHE ENTWICKLUNG EINES EINHEITLICHEN VORGEHENSMODELLS 4 EVALUATION 4 1 DEMONSTRATION DES ARTEFAKTS 4 2 EVALUATION DES ARTEFAKTS 5 SCHLUSSBETRACHTUNG 5 1 ZUSAMMENFASSUNG DER ERKENNTNISSE 5 2 KRITISCHE W RDIGUNG 5 3 AUSBLICK Diese Arbeit konzentriert sich auf die Funktionalit t des digitalen Zwillings den aktuellen Status des physischen Zwillings darzustellen was durch dessen Datenerfassung erm glicht wird Dazu soll im Kern der Arbeit ein Demonstrator entwickelt werden der die Wirkungsweise und Funktionsweise von Digital Twins demonstriert und veranschaulicht Es ist eine Methode f r die Entwicklung von Digital Twins als Demonstratoren zu entwickeln in deren Rahmen ein konkreter Digital Twin implementiert wird Daraus ergibt sich folgende Forschungsfrage die im Rahmen dieser Masterarbeit untersucht werden soll Wie kann ein Vorgehensmodell gestaltet sein das die Entwicklung eines Digital Twin Demonstrators beschreibt Als Methode zur Beantwortung der Forschungsfrage wird der wissenschaftliche Ansatz der Design Science Research verwendet der unter anderem von March und Smith 1995 sowie Hevner et al 2004 beschrieben wird Nach Hevner et al 2004 befasst sich die Design Science mit der Forschung durch die Entwicklung und Bewertung von Artefakten die den identifizierten Unternehmensbedarf decken sollen Braun et al sind der Auffassung dass nach dem Ansatz der Grundlagenforschung die Methodenkonstruktion eindeutig Teil der Design Science ist Damit belegen sie dass die Konzeption eines Vorgehensmodells so wie es im Rahmen dieser Arbeit entwickelt werden soll als denkbares Artefakt dieser Herangehensweise wahrzunehmen ist Neben den von Hevner et al 2004 ver ffentlichen Richtlinien zur Design Science Research orientiert sich diese Masterarbeit an dem Vorgehensmodell von Peffers et al 2006 **Government Reports Annual Index** ,1991 **Technical Book Review Index** ,1971 **Digital Logic Design** Guy Even,Moti Medina,2012-10-08 This textbook based on the authors fifteen years of teaching is a complete teaching tool for turning students into logic designers in one semester Each chapter describes new concepts giving extensive applications and examples Assuming no prior knowledge of discrete mathematics the authors introduce all background in propositional logic asymptotics graphs hardware and electronics Important features of the presentation are All material is presented in full detail Every designed circuit is formally specified and implemented the correctness of the implementation is proved and the cost and delay are analyzed Algorithmic solutions are offered for logical simulation computation of propagation delay and minimum clock period Connections are drawn from the physical analog world to the digital abstraction The language of graphs is used to describe formulas and circuits Hundreds of figures examples and exercises enhance understanding The extensive website http www eng tau ac il guy Even Medina includes teaching slides links to Logisim and a DLX assembly simulator **Host Bibliographic Record for Boundwith Item Barcode 30112118442471 and Others** ,2013 Library of Congress Catalog Library of Congress,1970 A cumulative list of works represented by Library of Congress printed cards **Literature on Automation** ,1967 *Handbuch der Mediengeschichte* Helmut Schanze,2001

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