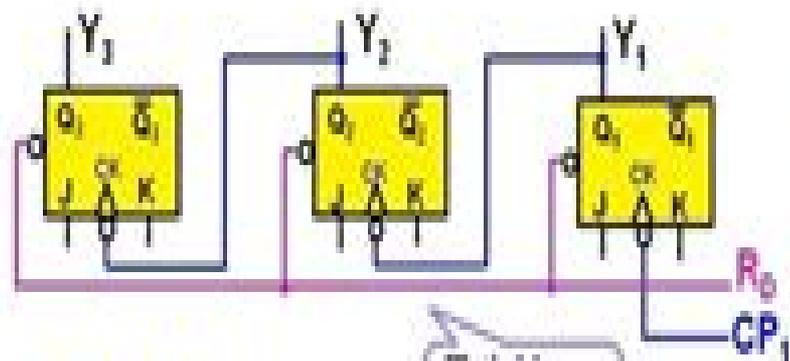


# Digital Logic Design - Asynchronous Counter

## ③ 状态转换表



## ① 输入方程

$$J_1 = K_1 = 1 \quad CP_1 \downarrow$$

$$J_2 = K_2 = 1 \quad CP_2 = Y_1 \downarrow$$

$$J_3 = K_3 = 1 \quad CP_3 = Y_2 \downarrow$$

异步Moor  
型时序电路

异步模8加  
法计数器

| 现态      |         |         | 次态          |             |             | 时钟     |        |        |
|---------|---------|---------|-------------|-------------|-------------|--------|--------|--------|
| $Y_3^n$ | $Y_2^n$ | $Y_1^n$ | $Y_3^{n+1}$ | $Y_2^{n+1}$ | $Y_1^{n+1}$ | $CP_3$ | $CP_2$ | $CP_1$ |
| 0       | 0       | 0       | 0           | 0           | 1           | 无      | 无      | ↓      |
| 0       | 0       | 1       | 0           | 1           | 0           | 无      | ↓      | ↓      |
| 0       | 1       | 0       | 0           | 1           | 1           | 无      | 无      | ↓      |
| 0       | 1       | 1       | 1           | 0           | 0           | ↓      | ↓      | ↓      |
| 1       | 0       | 0       | 1           | 0           | 1           | 无      | 无      | ↓      |
| 1       | 0       | 1       | 1           | 1           | 0           | 无      | ↓      | ↓      |
| 1       | 1       | 0       | 1           | 1           | 1           | 无      | 无      | ↓      |
| 1       | 1       | 1       | 0           | 0           | 0           | ↓      | ↓      | ↓      |

## ② 次态方程

$$Y_1^{n+1} = J_1 \bar{Q}_1 + \bar{K}_1 Q_1 = \bar{Y}_1 \quad CP_1 \downarrow$$

$$Y_2^{n+1} = J_2 \bar{Q}_2 + \bar{K}_2 Q_2 = \bar{Y}_2 \quad Y_1 \downarrow$$

$$Y_3^{n+1} = J_3 \bar{Q}_3 + \bar{K}_3 Q_3 = \bar{Y}_3 \quad Y_2 \downarrow$$

## ④ 波形图

二分频



# Digital Systems Design With Programmable Logic

**Parag K. Lala**



## **Digital Systems Design With Programmable Logic:**

**Digital Systems Design and Prototyping** Zoran Salcic, Asim Smailagic, 2000-10-31 A book CD ROM covering digital systems design using two important technologies field programmable logic devices FPLDs and hardware description languages HDLs These two technologies are combined to aid in the design prototyping and implementation of a range of digital systems Presents VHDL and Verilog widely used standard languages and the proprietary Altera HDL Chapters on these languages serve as tutorials and comparisons are made to highlight strengths and weaknesses of each language The CD ROM contains the Altera MAX PLUS II development environment Can be used as a reference or an advanced level text Salcic is affiliated with the University of Auckland Smailagic is affiliated with Carnegie Mellon University Annotation copyrighted by Book News Inc Portland OR

**Digital Systems Design with FPGAs and CPLDs** Ian Grout, 2011-04-08 Digital Systems Design with FPGAs and CPLDs explains how to design and develop digital electronic systems using programmable logic devices PLDs Totally practical in nature the book features numerous quantify when known case study designs using a variety of Field Programmable Gate Array FPGA and Complex Programmable Logic Devices CPLD for a range of applications from control and instrumentation to semiconductor automatic test equipment Key features include Case studies that provide a walk through of the design process highlighting the trade offs involved Discussion of real world issues such as choice of device pin out power supply power supply decoupling signal integrity for embedding FPGAs within a PCB based design With this book engineers will be able to Use PLD technology to develop digital and mixed signal electronic systems Develop PLD based designs using both schematic capture and VHDL synthesis techniques Interface a PLD to digital and mixed signal systems Undertake complete design exercises from design concept through to the build and test of PLD based electronic hardware This book will be ideal for electronic and computer engineering students taking a practical or Lab based course on digital systems development using PLDs and for engineers in industry looking for concrete advice on developing a digital system using a FPGA or CPLD as its core Case studies that provide a walk through of the design process highlighting the trade offs involved Discussion of real world issues such as choice of device pin out power supply power supply decoupling signal integrity for embedding FPGAs within a PCB based design

Digital Systems Design and Prototyping Using Field Programmable Logic Zoran Salcic, Asim Smailagic, 2014-01-15

**Digital System Design with FPGA: Implementation Using Verilog and VHDL** Cem Unsalan, Bora Tar, 2017-07-14 Master FPGA digital system design and implementation with Verilog and VHDL This practical guide explores the development and deployment of FPGA based digital systems using the two most popular hardware description languages Verilog and VHDL Written by a pair of digital circuit design experts the book offers a solid grounding in FPGA principles practices and applications and provides an overview of more complex topics Important concepts are demonstrated through real world examples ready to run code and inexpensive start to finish projects for both the Basys and Arty boards Digital System Design with FPGA Implementation

Using Verilog and VHDL covers Field programmable gate array fundamentals Basys and Arty FPGA boards The Vivado design suite Verilog and VHDL Data types and operators Combinational circuits and circuit blocks Data storage elements and sequential circuits Soft core microcontroller and digital interfacing Advanced FPGA applications The future of FPGA

*Design of Digital Systems and Devices* Marian Adamski,Alexander Barkalov,Marek Wegrzyn,2011-02-04 Logic design of digital devices is a very important part of the Computer Science It deals with design and testing of logic circuits for both data path and control unit of a digital system Design methods depend strongly on logic elements using for implementation of logic circuits Different programmable logic devices are wide used for implementation of logic circuits Nowadays we witness the rapid growth of new and new chips but there is a strong lack of new design methods This book includes a variety of design and test methods targeted on different digital devices It covers methods of digital system design the development of theoretical base for construction and designing of the PLD based devices application of UML for digital design A considerable part of the book is devoted to design methods oriented on implementing control units using FPGA and CPLD chips Such important issues as design of reliable FSMs automatic design of concurrent logic controllers the models and methods for creating infrastructure IP services for the SoCs are also presented The editors of the book hope that it will be interesting and useful for experts in Computer Science and Electronics as well as for students who are viewed as designers of future digital devices and systems Digital Systems Design With Fpgas And Cplds Ian Grout,2010-01-01 Digital Systems Design with

Programmable Logic Martin Bolton,1990 Hardware Logic Design **Digital Systems Design and Prototyping Using Field Programmable Logic** Zoran Salcic,Asim Smailagic,1999 **Design of Digital Systems and Devices** Marian Adamski,Alexander Barkalov,Marek Wegrzyn,2011-01-27 Logic design of digital devices is a very important part of the Computer Science It deals with design and testing of logic circuits for both data path and control unit of a digital system Design methods depend strongly on logic elements using for implementation of logic circuits Different programmable logic devices are wide used for implementation of logic circuits Nowadays we witness the rapid growth of new and new chips but there is a strong lack of new design methods This book includes a variety of design and test methods targeted on different digital devices It covers methods of digital system design the development of theoretical base for construction and designing of the PLD based devices application of UML for digital design A considerable part of the book is devoted to design methods oriented on implementing control units using FPGA and CPLD chips Such important issues as design of reliable FSMs automatic design of concurrent logic controllers the models and methods for creating infrastructure IP services for the SoCs are also presented The editors of the book hope that it will be interesting and useful for experts in Computer Science and Electronics as well as for students who are viewed as designers of future digital devices and systems **Digital System Design Using Programmable Logic Devices** Lala,1990 *Digital Design and Implementation with Field Programmable Devices* Zainalabedin Navabi,2004-05 The focus of Digital Design and Implementation with Field Programmable Devices is on

a practical knowledge of digital system design for programmable devices The book covers all necessary topics under one cover and covers each topic just enough that is actually used by an advanced digital designer The book is broken into three sections covering digital system design concepts use of tools and systematic design of digital systems This book provides a recap of digital design topics and computer architectures and shows the Verilog language for synthesis In addition for an industrial setting the book shows how existing design components are used in upper level designs and how user libraries are formed and utilized Using Altera s UP2 programmable device development board with this book helps engineers test and debug their designs before programming their programmable devices on production boards In an educational setting the book can be used as a complementary book for the basic logic design course or a laboratory book for the sophomore logic design lab or as a textbook for senior level design courses Using Altera s UP2 programmable device education board with this book helps students see their designs being implemented and tested and thereby get a down to wire understanding of how things work

**Digital Systems Design with Programmable Logic** Jude Moore,2014-12-18 This course introduces digital systems design with hardware description languages HDL programmable implementation technologies electronic design automation design flows design considerations and constraints design for test system on a chip designs IP cores reconfigurable computing digital system design examples and applications Digital Systems Design Using VHDL Charles H. Roth,Lizy K. John,2008 This textbook is intended for a senior level course in digital systems design The book covers both basic principles of digital systems design and the use of a hardware description language VHDL in the design process

*DIGITAL SYSTEM DESIGN AND APPLICATIONS* Dr.P.Balakrishna ,Dr.Minal Sudarshan Hardas ,Dr.Sandeep Kumar Vasa ,Mr.Arun Raj S.R,2025-11-24 Digital systems form the backbone of modern technology powering everything from computers and communication networks to embedded controllers medical instruments automation systems and intelligent devices With rapid advancements in digital electronics there is a growing need for comprehensive learning resources that combine theoretical foundations with practical design approaches This multi author book Digital System Design and Applications has been developed to meet that need by offering a clear structured and application oriented understanding of digital systems The objective of this book is to provide students educators and professionals with a solid foundation in digital logic combinational and sequential circuits finite state machines programmable logic devices Verilog VHDL modeling FPGA based design and real time applications Each chapter is written by academic experts and researchers who bring their specialized knowledge and experience to the topic ensuring depth clarity and relevance As a multi author academic volume the book reflects the collaborative efforts of contributors from diverse engineering backgrounds including digital electronics computer engineering embedded systems and VLSI design Their combined expertise enriches the content with practical insights real world examples design techniques and implementation strategies widely used in industry and research Special emphasis has been placed on connecting fundamental concepts with hands on applications Readers will find clear explanations diagrams

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*Digital System Design - Use of Microcontroller* Shenouda Dawoud,R. Peplow,2022-09-01 Embedded systems are today widely deployed in just about every piece of machinery from toasters to spacecraft Embedded system designers face many challenges They are asked to produce increasingly complex systems using the latest technologies but these technologies are changing faster than ever They are asked to produce better quality designs with a shorter time to market They are asked to implement increasingly complex functionality but more importantly to satisfy numerous other constraints To achieve the current goals of design the designer must be aware with such design constraints and more importantly the factors that have a direct effect on them One of the challenges facing embedded system designers is the selection of the optimum processor for the application in hand single purpose general purpose or application specific Microcontrollers are one member of the family of the application specific processors The book concentrates on the use of microcontroller as the embedded system s processor and how to use it in many embedded system applications The book covers both the hardware and software aspects needed to design using microcontroller The book is ideal for undergraduate students and also the engineers that are working in the field of digital system design

Contents Preface Process design metrics A systems approach to digital system design Introduction to microcontrollers and microprocessors Instructions and Instruction sets Machine language and assembly language System memory Timers counters and watchdog timer Interfacing to local devices peripherals Analogue data and the analogue I O subsystem Multiprocessor communications Serial Communications and Network based interfaces

**CRC Handbook of Digital System Design, Second Edition** Wen C. Lin,1990-09-25 As technology advances digital system designers must acquire and maintain skills to design systems with analog pulse time and digital circuits along with LSI and VLSI devices The CRC Handbook of Digital System Design Second Edition reviews the fundamentals of these topics for the convenience of designers who need to refresh their memories from time to time In a somewhat unique presentation this book integrates theory with practical design and covers three broad topics The basics formulas design equation terminology symbols and notation Characterstics properties and principles of operation of devices modules and building blocks frequently used as components in digital system design Design procedures guidelines for system design presented through examples The author includes numerous examples both simple and complex

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**VHDL and FPLDs in Digital Systems Design, Prototyping and Customization** Zoran Salcic,1998-04-30 This book represents an attempt to treat three aspects of digital systems design prototyping and customization in an integrated manner using two major technologies VHSIC Hardware Description Language VHDL as a modeling and specification tool and Field Programmable Logic Devices FPLDs as an implementation technology They together make a very powerful combination for complex digital systems rapid design and prototyping as the important steps towards manufacturing or in the case of feasible quantities they also provide fast system manufacturing Combining these two technologies makes possible implementation of very complex digital systems at the desk VHDL has become a standard tool to capture features of digital systems in a form of behavioral dataflow or structural models providing a high degree of flexibility When augmented by a good simulator VHDL enables extensive verification of features of the system under design reducing uncertainties at the latter phases of design process As such it becomes an unavoidable modeling tool to model digital systems at various levels of abstraction Digital System Design Using VHDL Rishabh Anand,2023 The book covers the complete syllabus of subject as suggested by most of the universities in India Generic VHDL code is taught and used through out the book so that different companies VHDL tools can be used if desired Moving from the unknown in a logical manner Subject matter in each chapter develops systematically from inceptions Large number of carefully selected worked examples in sufficient details No other reference is required Ideally suited for self study *Digital System Design Using Programmable Logic Devices* Parag K. Lala,1990 Describes and compares many programmable logic devices which have become popular as building blocks for digital systems from different manufacturers and provides design examples to demonstrate their application Emphasizes learning how to implement logic systems from specifications using PLDs Intended for advanced level academic and professional **Digital System Design Using Programmable Logic Design** Parag K. Lala,1990

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