

The background of the cover is a detailed, etched-style diagram of a microprocessor chip. It shows a complex network of lines representing data and control buses, along with various functional blocks. Labels include 'DATA BUS', 'CONTROL BUS', 'ADDRESS BUS', 'ROM', 'RAM', 'CPU', and 'I/O'. The diagram is rendered in a light, metallic color against a darker, textured background.

BIT-SLICE MICROPROCESSOR DESIGN

JOHN MICK AND JIM BRICK

Bit Slice Microprocessor Design

Wade H. Shafer



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Space Operations United States. General Accounting Office, 1989

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