

Verilog

21/11/24

- Mobile phone
 - It contains billion of transistors
- We required a language to do all this automation and EDA tool
 - EDA: Electronic device automation
 - ex: Questa, Synopsys, Cadence, Ver
- Logical gate AND
- Software languages: C, C++, Java
- Hardware device
 - required one language which supports hardware behaviour
- we have 2 languages:
 - Verilog and VHDL
- How hardware languages are diff from software languages.
 - hardware languages supports concepts of hardware
 - hardware languages supports concepts of time and state
 - " " " concurrent running process.

Syntax:

```
module tpraptop (input - charg - port, output - audio - port);  
  // logic  
endmodule
```

- * Verilog vs VHDL
- Verilog have less no. of constructs
- Verilog is high level language
- Verilog is easier to understand
- * Verilog concepts

Complete Verilog

RM Cervero



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