

Customer Success Story

Implementing Advanced Formal Verification Methodology

The Customer Challenge

Valens, a leading provider of HDBaseT connectivity solutions, needed to enhance their verification flow to improve design quality and productivity. The challenge was to introduce Formal Verification methodology into their existing processes while maintaining ongoing development activities. This required implementing new verification approaches across multiple critical components, including their core HDBaseT packet interface protocol, Custom RISC microcontroller, and multi-port Queue Manager.

The Veriest Solution

Veriest Solutions provided comprehensive formal verification expertise:

- Defined and implemented new Formal Verification methodology
- Created Formal VIP for HDBaseT packet interface protocol
- Extended verification coverage for Custom RISC microcontroller
- Implemented formal methods for multi-port Queue Manager
- Integrated formal verification into existing design flows
- Enabled knowledge transfer for methodology adoption

Result

The partnership delivered outstanding outcomes:

- Successfully integrated Formal Verification into existing flows
- Achieved higher verification coverage with reduced resources
- Improved overall design quality and productivity
- Minimized disruption to ongoing development activities
- Established foundation for continued formal verification usage
- Created verification methodology showcased at CDNLive Israel



"We were extremely satisfied with the services we received from Veriest, whose expertise was critical in our efforts to introduce Formal Verification. We were able to quickly add this tool to our current methodology and achieve great results – with minimum disruption to our daily activities."

Itamar Nitzan, Verification Manager at Valens

Advanced Formal Verification

Thomas Kropf



Advanced Formal Verification:

Advanced Formal Verification Rolf Drechsler, 2007-05-08 Advanced Formal Verification shows the latest developments in the verification domain from the perspectives of the user and the developer World leading experts describe the underlying methods of today's verification tools and describe various scenarios from industrial practice In the first part of the book the core techniques of today's formal verification tools such as SAT and BDDs are addressed In addition multipliers which are known to be difficult are studied The second part gives insight in professional tools and the underlying methodology such as property checking and assertion based verification Finally analog components have to be considered to cope with complete system on chip designs

Advanced Formal Verification Rolf Drechsler, 2014-01-15 **Professional Verification** Paul Wilcox, 2004-05-13 Professional Verification is a guide to advanced functional verification in the nanometer era It presents the best practices in functional verification used today and provides insights on how to solve the problems that verification teams face Professional Verification is based on the experiences of advanced verification teams throughout the industry along with work done at Cadence Design Systems Professional Verification presents a complete and detailed Unified Verification Methodology based on the best practices in use today It also addresses topics important to those doing advanced functional verification such as assertions functional coverage formal verification and reactive testbenches

Combining Advanced Formal Hardware Verification Techniques Erik Henry Reeber, 2007 This dissertation combines formal verification techniques in an attempt to reduce the human effort required to verify large systems formally One method to reduce the human effort required by formal verification is to modify general purpose theorem proving techniques to increase the number of lemma instances considered automatically Such a modification to the forward chaining proof technique within the ACL2 theorem prover is described This dissertation identifies a decidable subclass of the ACL2 logic the Subclass of Unrollable List Formulas in ACL2 SUFLA SUFLA is shown to be decidable i.e. there exists an algorithm that decides whether any SUFLA formula is valid Theorems from first order logic can be proven through a methodology that combines interactive theorem proving with a fully automated solver for SUFLA formulas This methodology has been applied to the verification of components of the TRIPS processor a prototype processor designed and fabricated by the University of Texas and IBM Also a fully automated procedure for the Satisfiability Modulo Theory SMT of bit vectors is implemented by combining a solver for SUFLA formulas with the ACL2 theorem prover's general purpose rewriting proof technique A new methodology for combining theorem proving and model checking is presented which uses a unique black box formalization of hardware designs This methodology has been used to combine the ACL2 theorem prover with IBM's SixthSense model checker and applied to the verification of a high performance industrial multiplier design A general purpose mechanism has been created for adding external tools to a general purpose theorem prover This mechanism implemented in the ACL2 theorem prover is capable of supporting the combination of ACL2 with both SixthSense and the SAT based SUFLA solver A new hardware

description language DE2 is described DE2 has a number of unique features geared towards simplifying formal verification including a relatively simple formal semantics support for the description of circuit generators and support for embedding non functional constructs within a hardware design The composition of these techniques extend our knowledge of the languages and logics needed for formal verification and should reduce the human effort required to verify large hardware circuit models

Formal Verification Erik Seligman, Tom Schubert, M. V. Achutha Kiran Kumar, 2023-05-26 Formal Verification An Essential Toolkit for Modern VLSI Design Second Edition presents practical approaches for design and validation with hands on advice to help working engineers integrate these techniques into their work Formal Verification FV enables a designer to directly analyze and mathematically explore the quality or other aspects of a Register Transfer Level RTL design without using simulations This can reduce time spent validating designs and more quickly reach a final design for manufacturing Building on a basic knowledge of SystemVerilog this book demystifies FV and presents the practical applications that are bringing it into mainstream design and validation processes Every chapter in the second edition has been updated to reflect evolving FV practices and advanced techniques In addition a new chapter Formal Signoff on Real Projects provides guidelines for implementing signoff quality FV completely replacing some simulation tasks with significantly more productive FV methods After reading this book readers will be prepared to introduce FV in their organization to effectively deploy FV techniques that increase design and validation productivity

Advanced Techniques for Assertion-Based Verification in Hardware Designs Using Data Mining Algorithms Mohammad Reza Heidari Iman, 2025-08-02 This book introduces leading edge techniques for verifying the complex electronic systems used in industries such as aerospace automotive and medical devices and ensuring the safety and security of these systems By focusing on advanced verification and security verification methods the author addresses the critical need to detect and prevent potential bugs errors and vulnerabilities such as Hardware Trojans in embedded systems With an emphasis on innovative approaches to assertion based verification this book provides valuable insights for engineers researchers and professionals dedicated to enhancing the functional verification security and trustworthiness of critical technological systems The methods described in this book address key shortcomings in current automatic assertion miners used for assertion based verification such as long execution times excessive and redundant assertion generation and inconsistency among generated assertions The author discusses several innovative methods tools and techniques such as ARTmine IMMizer and Dominance which enhance functional verification and facilitate the automatic generation evaluation and minimization of assertions Additionally novel techniques are introduced for security verification including a security based assertion miner for RISC V processors and ADAssure for debugging and bug localization in autonomous driving control algorithms of autonomous vehicles

Formal Verification Erik Seligman, Tom Schubert, M V Achutha Kiran Kumar, 2023-05-26 Formal Verification An Essential Toolkit for Modern VLSI Design Second Edition presents practical approaches for design and validation with hands

on advice to help working engineers integrate these techniques into their work Formal Verification FV enables a designer to directly analyze and mathematically explore the quality or other aspects of a Register Transfer Level RTL design without using simulations This can reduce time spent validating designs and more quickly reach a final design for manufacturing Building on a basic knowledge of SystemVerilog this book demystifies FV and presents the practical applications that are bringing it into mainstream design and validation processes Every chapter in the second edition has been updated to reflect evolving FV practices and advanced techniques In addition a new chapter Formal Signoff on Real Projects provides guidelines for implementing signoff quality FV completely replacing some simulation tasks with significantly more productive FV methods After reading this book readers will be prepared to introduce FV in their organization to effectively deploy FV techniques that increase design and validation productivity Covers formal verification algorithms that help users gain full coverage without exhaustive simulation Helps readers understand formal verification tools and how they differ from simulation tools Shows how to create instant testbenches to gain insights into how models work and to find initial bugs Presents insights from Intel insiders who share their hard won knowledge and solutions to complex design problems

13th International Conference on Information Systems and Advanced Technologies “ICISAT 2023” Mohamed Ridda Laouar,Valentina Emilia Balas,Vincenzo Piuri,Dana Rad,Zineb Touati Hamad,Abbas Cheddad,2024-07-30 This book covers the Proceedings of the 13th International Conference on Information Systems and Advanced Technologies ICISAT 2023 One of the evocative and valuable dimensions of this conference is the way it brings together researchers scientists academics and engineers in the field from different countries and enables discussions and debate of relevant issues challenges opportunities and research findings The ICISAT 2023 conference provided a forum for research and developments in the field of information systems and advanced technologies and new trends in developing information systems organizational aspects of their development and intelligent aspects of the final product The aim of the ICISAT 2023 is to report progress and development of methodologies technologies planning and implementation tools and standards in information systems technologies and sciences ICISAT 2023 aims at addressing issues related to the intelligent information data science and decision support system from multidisciplinary perspectives and to discuss the research teaching and professional practice in the field The book of ICISAT 2023 includes selected papers from the 13th International Conference on Information Systems and Advanced Technologies ICISAT 2023 organized during December 29 30 2023 In this book researchers professional software and systems engineers from around the world addressed intelligent information data science and decision support system for the conference The ideas and practical solutions described in the book are the outcome of dedicated research by academics and practitioners aiming to advance theory and practice in this research domain The list of topics is in all the areas of modern intelligent information systems and technologies such as neural networks evolutionary computing adaptive systems pervasive system ubiquitous system E learning and teaching knowledge based

paradigms learning paradigms intelligent data analysis intelligent decision making and support system intelligent network security web intelligence deep learning natural language processing image processing general machine learning and unsupervised learning

Introduction to Formal Hardware Verification Thomas Kropf,1999-10-16 This advanced textbook presents an almost complete overview of techniques for hardware verification It covers all approaches used in existing tools such as binary and word level decision diagrams symbolic methods for equivalence and temporal logic model checking and introduces the use of higher order logic theorem proving for verifying circuit correctness Each chapter contains an introduction and a summary as well as a section for the advanced reader aiding an understanding of the advantages and limitations of each technique Backed by many examples and illustrations this text will appeal to a broad audience from beginners in system design to experts XXXXXXXX Neuer Text This is a complete overview of existing techniques for hardware verification It covers all approaches used in existing verification tools such as symbolic methods for equivalence checking temporal logic model checking and higher order logic theorem proving for verifying circuit correctness The book helps readers to understand the advantages and limitations of each technique Each chapter contains a summary as well as a section for the advanced reader

Advanced Methods and Technologies for Agent and Multi-Agent Systems D. Barbusca,M.T. Le,R.J. Howlett,2013-05-14 The field of agent and multi agent systems is concerned with the development and evaluation of sophisticated AI based problem solving and control architectures for both single and multi agent systems This book presents the proceedings of the 7th KES Conference on Agent and Multi agent Systems Technologies and Applications KES AMSTA 2013 held in Hue City Vietnam in May 2013 The KES AMSTA 2013 conference provides an internationally respected forum for scientific research in the technologies and applications of agent and multi agent systems In all 44 papers were selected for oral presentation and publication in this volume Special attention is paid to the feature topics of intelligent technologies and applications in the area of e health social networking self organizing systems economics and trust management Other topics covered include agent oriented software engineering beliefs engineering desires and intentions representation agent cooperation coordination negotiation organization and communication distributed problem solving specification of agent communication languages formalization of ontologies and conversational agents The book highlights new trends and challenges in agent and multi agent research and will be of interest to the research community working in the fields of artificial intelligence collective computational intelligence robotics dialogue systems and in particular agent and multi agent systems technologies and applications

Innovations and Advanced Techniques in Computer and Information Sciences and Engineering Tarek Sobh,2007-09-04 Innovations and Advanced Techniques in Computer and Information Sciences and Engineering includes a set of rigorously reviewed world class manuscripts addressing and detailing state of the art research projects in the areas of Computer Science Computer Engineering and Information Sciences Innovations and Advanced Techniques in Computer and Information Sciences and Engineering includes selected papers form

the conference proceedings of the International Conference on Systems Computing Sciences and Software Engineering SCSS 2006 which was part of the International Joint Conferences on Computer Information and Systems Sciences and Engineering CISSE 2006 All aspects of the conference were managed on line not only the reviewing submissions and registration processes but also the actual conference Conference participants authors presenters and attendees only needed an internet connection and sound available on their computers in order to be able to contribute and participate in this international ground breaking conference The on line structure of this high quality event allowed academic professionals and industry participants to contribute work and attend world class technical presentations based on rigorously refereed submissions live without the need for investing significant travel funds or time out of the office Suffice to say that CISSE received submissions from more than 70 countries for whose researchers this opportunity presented a much more affordable dynamic and well planned event to attend and submit their work to versus a classic on the ground conference The CISSE conference audio room provided superb audio even over low speed internet connections the ability to display PowerPoint presentations and cross platform compatibility the conferencing software runs on Windows Mac and any other operating system that supports Java In addition the conferencing system allowed for an unlimited number of participants which in turn granted CISSE the opportunity to allow all participants to attend all presentations as opposed to limiting the number of available seats for each session

Advanced Automation in Formal Verification of Processors Ulrich Kühne,2009 *Formal Verification of Advanced Families of Security Protocols* Cyrille Wiedling,2014 Formal methods have been used to analyze security protocols and several tools have even been developed to tackle automatically different proof techniques and ease the verification of such protocols However for electronic voting and APIs current tools tend to reach their limits because they can t handle some cryptographic primitives or the security properties involved in those protocols We work on two cases studies of existing and deployed systems a Norwegian e voting protocol and a CNRS boardroom voting protocol We analyze them using the applied pi calculus model and we discuss in details about their security properties in different corruption scenarios Even including several reusable results these proofs are complex and therefore expose a real need for automation Thus we focus on a possible lead in direction of this needed automation type systems We build upon a recent work describing a new type system designed to deal with equivalence properties in order to apply this on the verification of equivalence based properties in electronic voting like ballot secrecy We present an application of this method through Helios a well known e voting system Another family of advanced security protocols are APIs secure interfaces devoted to allow access to some information stored into a secured trusted hardware without leaking it outside Recet work seems to show that these interfaces are also vulnerable In this thesis we provide a new design for APIs including revocation In addition we include a formal analysis of this API showing that a malicious combination of API s commands does not leak any key even when the adversary may brute force some of them

Formal System Verification Rolf Drechsler,2017-06-21 This book provides readers with a

comprehensive introduction to the formal verification of hardware and software World leading experts from the domain of formal proof techniques show the latest developments starting from electronic system level ESL descriptions down to the register transfer level RTL The authors demonstrate at different abstraction layers how formal methods can help to ensure functional correctness Coverage includes the latest academic research results as well as descriptions of industrial tools and case studies *Shortening the Path from Specification to Prototype* ,2004 *Formal verification of an advanced pipelined machine* Jun Sawada,1999 Advanced CISSP Prep Guide Ronald L. Krutz,Russell Dean Vines,2002-10-18 Get ready to pass the CISSP exam and earn your certification with this advanced test guide Used alone or as an in depth supplement to the bestselling *The CISSP Prep Guide* this book provides you with an even more intensive preparation for the CISSP exam With the help of more than 300 advanced questions and detailed answers you ll gain a better understanding of the key concepts associated with the ten domains of the common body of knowledge CBK Each question is designed to test you on the information you ll need to know in order to pass the exam Along with explanations of the answers to these advanced questions you ll find discussions on some common incorrect responses as well In addition to serving as an excellent tutorial this book presents you with the latest developments in information security It includes new information on Carnivore Echelon and the U S Patriot Act The Digital Millennium Copyright Act DMCA and recent rulings The European Union Electronic Signature Directive The Advanced Encryption Standard biometrics and the Software Capability Maturity Model Genetic algorithms and wireless security models New threats and countermeasures The CD ROM includes all the questions and answers from the book with the Boson powered test engine **Seventeenth Conference on Advanced Research in VLSI** Richard B. Brown,Alexander T. Ishii,1997 This volume on computational intelligence covers the 17th Conference on Advanced Research in VLSI in 1997 Topics include VLSI architecture asynchronous design circuits layout image sensors optimization system timing CAD and low power design XIII SBMicro, International Conference on Microelectronics and Packaging, ICMP'98: Advanced courses and invited papers Ivan Jorge Chueiri,Carlos Alberto dos Reis Filho,1998

SAT-Based Scalable Formal Verification Solutions Malay Ganai,Aarti Gupta,2007-05-26 Functional verification has become an important aspect of the chip design process Significant resources both in industry and academia are devoted to the design complexity and verification endeavors SAT Based Scalable Formal Verification Solutions discusses in detail several of the latest and interesting scalable SAT based techniques including Hybrid SAT Solver Customized Bounded Unbounded Model Checking Distributed Model Checking Proofs and Proof based Abstraction Methods Verification of Embedded Memory System Multi clock Systems and Synthesis for Verification Paradigm These techniques have been designed and implemented in a verification platform Verisol formally called DiVer and have been used successfully in industry This book provides algorithmic details and engineering insights into devising scalable approaches for an effective realization It also includes the authors practical experiences and recommendations in verifying the large industry designs

using VeriSol The book is primarily written for researchers scientists and verification engineers who would like to gain an in depth understanding of scalable SAT based verification techniques The book will also be of interest for CAD tool developers who would like to incorporate various SAT based advanced techniques in their products

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