



ADVANCES IN ELECTRONICS AND ELECTRON PHYSICS

Volume 65

Peter W. Hawkes

Advances In Electronics And Electron Physics Volume 68

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silicon as the preferred material and photo lithography as the fabrication technique to build binary devices transistors The construction of such devices yields gates that are able to perform Boolean operations and can be combined with computational systems capable of storing processing and transmitting digital signals encoded as electron currents and charges Since the invention of the integrated circuits microelectronics has reached increasing performances by decreasing strategically the size of its devices and systems an approach known as scaling down which simultaneously allow the devices to operate at higher speeds Provides a theory guided approach to the design of molecular and nano electronics Includes solutions for researchers working in this area Contributions from some of the most active researchers in the field of nano electronics *Proceedings of ION GPS-91* Institute of Navigation. Satellite Division. International Technical Meeting,1991

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