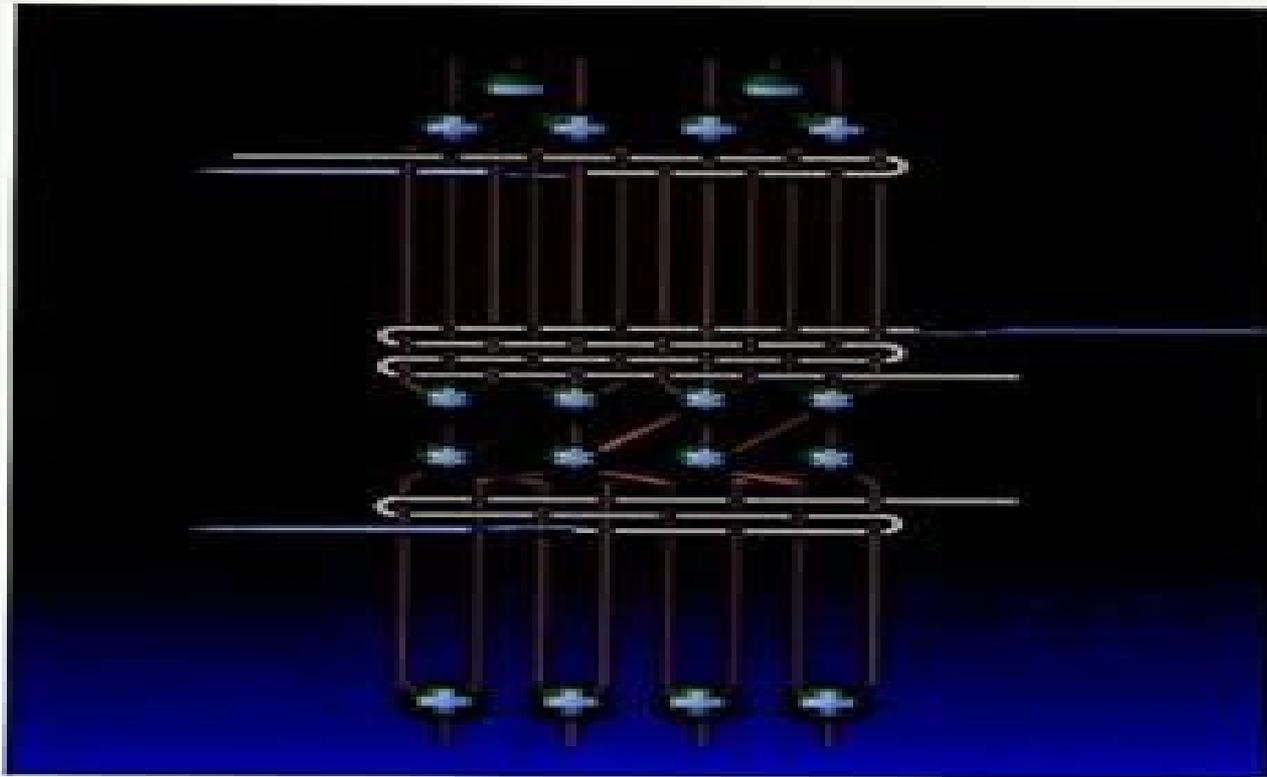


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Guang R. Gao, Lubomir Bic, and Jean-Luc Gaudiot



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High Performance Computing Alex Veidenbaum, 2003-10-09 This book constitutes the refereed proceedings of the 5th International Symposium on High Performance Computing ISHPC 2003 held in Tokyo Odaiba Japan in October 2003 The 23 revised full papers and 16 short papers presented together with 4 invited papers and 7 refereed papers accepted for a concurrently held workshop on OpenMP WOMPEI 2003 were carefully reviewed and selected from 58 submissions The papers are organized in topical sections on architecture software applications and ITBL

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throughout their professional careers *Readings in Hardware/Software Co-Design* Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2001-06-19 Embedded system designers are constantly looking for new tools and techniques to help satisfy the exploding demand for consumer information appliances and specialized industrial products One critical barrier to the timely release of embedded system products is integrating the design of the hardware and software systems Hardware software co design is a set of methodologies and techniques specifically created to support the concurrent design of both systems effectively reducing multiple iterations and major redesigns In addition to its critical role in the development of embedded systems many experts believe that co design will be a key design methodology for Systems on a Chip *Readings in Hardware Software Co Design* presents the papers that have shaped the hardware software co design field since its inception in the early 90s Field experts Giovanni De Micheli Rolf Ernst and Wayne Wolf introduce sections of the book and provide context for the paper that follow This collection provides professionals researchers and graduate students with a single reference source for this critical aspect of computing design Over 50 peer reviewed papers written from leading researchers and designers in the field Selected edited and introduced by three of the fields most eminent researchers and educators Accompanied by an annually updated companion Web site with links and references to recently published papers providing a forum for the editors to comment on how recent work continues or breaks with previous work in the field **Parallel Architectures and Compilation Techniques**, 1997 **High Performance Computing**, 2003 **Annual ACM Symposium on Parallel Algorithms and Architectures**, 1997 **Research Directions in Parallel Functional Programming** Kevin Hammond, 1999-11 This volume provides a state of the art survey of research trends in parallel functional programming The text is divided into two sections the first section gives comprehensive introductions to key issues such as foundations programming constructs proof architectures and implementations the second comprises shorter summaries of research areas which are either of particular interest at the moment or which promise to provide key developments in the near future Topics covered here include coordination languages performance monitoring data flow programming explicit parallelism BSP and cost modelling Contributions have been commissioned by key researchers and practitioners in the area including several from the US and Canada where this is an area of increasing interest *Research Directions in Parallel Functional Programming* will be of interest to researchers post graduate students and practitioners in all relevant areas [Parallel and Distributed Processing](#), 1999 **High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures** K. J. Ray Liu, Kung Yao, 1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed architectures for VLSI

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Progress in Computer Research Frank H. Columbus,2001 Computer designers programmers and other specialists present 14 papers addressing a range of topics including parallel programming using visual patterns visualization of do loop performance using AVS an application framework for dynamic distributed software architectures parallel algorithms for multi dimensional integration and a new fast message passing communication system for multiprocessor workstation clusters Almost all the papers include an abstract list key words and include an introduction extensive discussion of the research conclusions suggestions for future work references and useful charts figures and diagrams c Book News Inc

Foundations of Parallel Programming Theodore Gyle Lewis,1993 Mathematics of Computing Parallelism

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