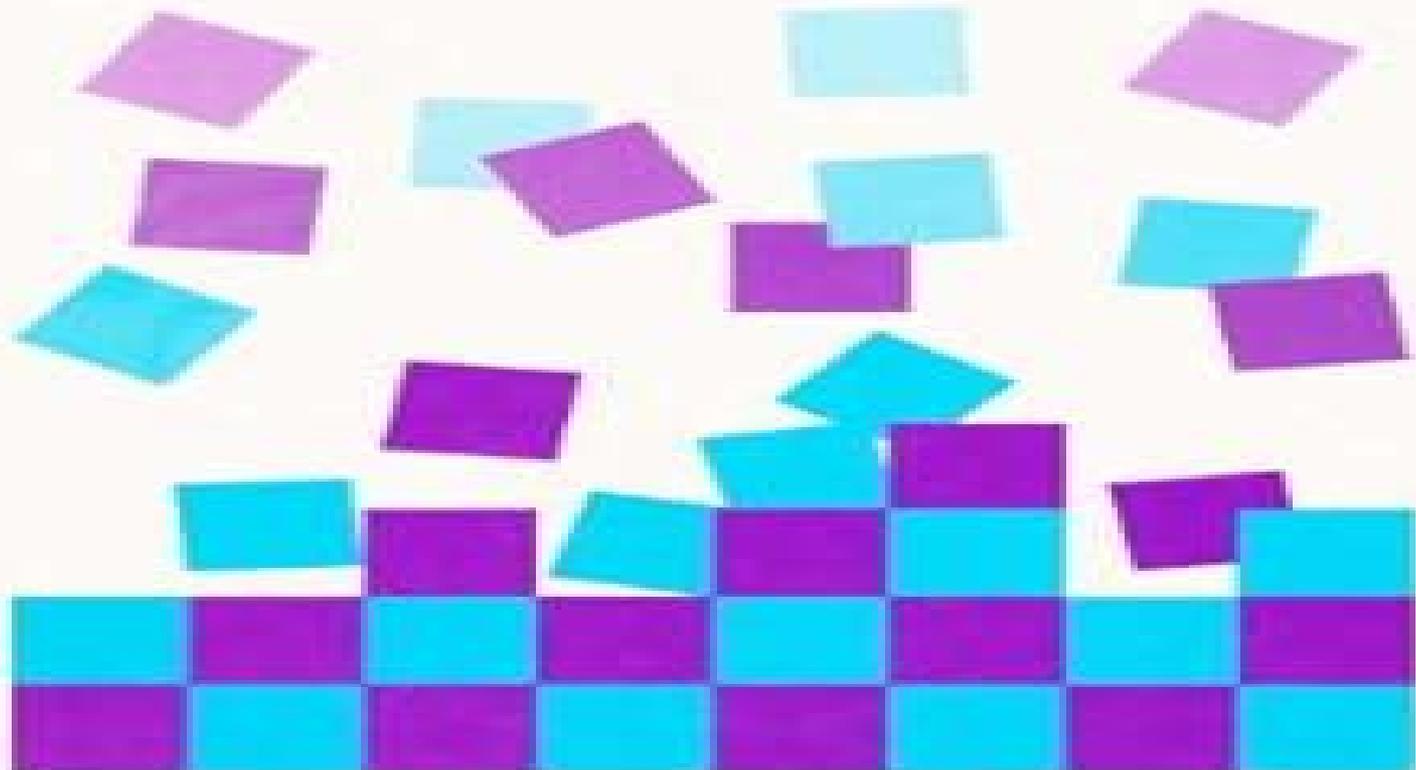


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Microprocessor Architecture and Programming William F. Leahy, 1977 This text is intended to aid in the educational transition process from the sphere of discrete electronic technologies to the medium and large scale integration techniques used in the microprocessor field The business manager or design engineer must weigh the cost of advanced technologies against the increased performance and marketability will find material of interest in the first three chapters Components of microprocessor systems and the design of microprocessor memory systems are the focus of the seven subsequent chapters The final five chapters focus on hardware and machine level programming using the Intel 8008 microprocessor machine language for the examples

Вычислительные Машины И Искусственный Интеллект, 1989

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1998 International Conference on Parallel Architectures and Compilation Techniques, Paris, France, October 12-18, 1998, 1998 This text on parallel architectures and compilation techniques covers such topics as loop transformations shared memory design techniques specialized multiprocessor systems parallel programming languages JAVA and multithreading processors register allocation and branch and value prediction

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