

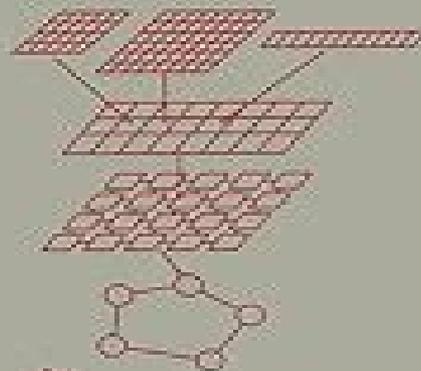
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1132

Guy-René Perrin Alain Darte (Eds.)

The Data Parallel Programming Model

Foundations, HPF Realization,
and Scientific Applications



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Data Parallel Programming Model Foundations Hpf Realization And Scientific Applications

Camilla Rothe



Data Parallel Programming Model Foundations Hpf Realization And Scientific Applications:

The Data Parallel Programming Model Guy-Rene Perrin,Alain Darte,1996-09-11 This monograph like book assembles the thoroughly revised and cross reviewed lectures given at the School on Data Parallelism held in Les Menuires France in May 1996 The book is a unique survey on the current status and future perspectives of the currently very promising and popular data parallel programming model Much attention is paid to the style of writing and complementary coverage of the relevant issues throughout the 12 chapters Thus these lecture notes are ideally suited for advanced courses or self instruction on data parallel programming Furthermore the book is indispensable reading for anybody doing research in data parallel programming and related areas [The Data Parallel Programming Model](#) Guy-Rene Perrin,Alain Darte,2014-03-12

This monograph like book assembles the thoroughly revised and cross reviewed lectures given at the School on Data Parallelism held in Les Menuires France in May 1996 The book is a unique survey on the current status and future perspectives of the currently very promising and popular data parallel programming model Much attention is paid to the style of writing and complementary coverage of the relevant issues throughout the 12 chapters Thus these lecture notes are ideally suited for advanced courses or self instruction on data parallel programming Furthermore the book is indispensable reading for anybody doing research in data parallel programming and related areas [User-Defined Tensor Data Analysis](#) Bin Dong,Kesheng Wu,Suren Byna,2021-09-29 The SpringerBrief introduces FasTensor a powerful parallel data programming model developed for big data applications This book also provides a user s guide for installing and using FasTensor FasTensor enables users to easily express many data analysis operations which may come from neural networks scientific computing or queries from traditional database management systems DBMS FasTensor frees users from all underlying and tedious data management tasks such as data partitioning communication and parallel execution This SpringerBrief gives a high level overview of the state of the art in parallel data programming model and a motivation for the design of FasTensor It illustrates the FasTensor application programming interface API with an abundance of examples and two real use cases from cutting edge scientific applications FasTensor can achieve multiple orders of magnitude speedup over Spark and other peer systems in executing big data analysis operations FasTensor makes programming for data analysis operations at large scale on supercomputers as productively and efficiently as possible A complete reference of FasTensor includes its theoretical foundations C implementation and usage in applications Scientists in domains such as physical and geosciences who analyze large amounts of data will want to purchase this SpringerBrief Data engineers who design and develop data analysis software and data scientists and who use Spark or TensorFlow to perform data analyses such as training a deep neural network will also find this SpringerBrief useful as a reference tool **Loop Tiling for Parallelism** Jingling Xue,2012-12-06 Loop tiling as one of the most important compiler optimizations is beneficial for both parallel machines and uniprocessors with a memory hierarchy This book explores the use of loop tiling for reducing communication

cost and improving parallelism for distributed memory machines The author provides mathematical foundations investigates loop permutability in the framework of nonsingular loop transformations discusses the necessary machineries required and presents state of the art results for finding communication and time minimal tiling choices Throughout the book theorems and algorithms are illustrated with numerous examples and diagrams The techniques presented in Loop Tiling for Parallelism can be adapted to work for a cluster of workstations and are also directly applicable to shared memory machines once the machines are modeled as BSP Bulk Synchronous Parallel machines Features and key topics Detailed review of the mathematical foundations including convex polyhedra and cones Self contained treatment of nonsingular loop transformations code generation and full loop permutability Tiling loop nests by rectangles and parallelepipeds including their mathematical definition dependence analysis legality test and code generation A complete suite of techniques for generating SPMD code for a tiled loop nest Up to date results on tile size and shape selection for reducing communication and improving parallelism End of chapter references for further reading Researchers and practitioners involved in optimizing compilers and students in advanced computer architecture studies will find this a lucid and well presented reference work with numerous citations to original sources

Dependability Engineering Fausto Pedro García Márquez, Mayorkinos Papaefthymiou, 2018-06-06 The new technology and system communication advances are being employed in any system being more complex The system dependability considers the technical complexity size and interdependency of the system The stochastic characteristic together with the complexity of the systems as dependability requires to be under control the Reliability Availability Maintainability and Safety RAMS The dependability contemplates therefore the faults failures downtimes stoppages worker errors etc Dependability also refers to emergent properties i e properties generated indirectly from other systems by the system analyzed Dependability understood as general description of system performance requires advanced analytics that are considered in this book Dependability management and engineering are covered with case studies and best practices The diversity of the issues will be covered from algorithms mathematical models and software engineering by design methodologies and technical or practical solutions This book intends to provide the reader with a comprehensive overview of the current state of the art case studies hardware and software solutions analytics and data science in dependability engineering

Reconfigurable Computing: Architectures, Tools and Applications Andreas Koch, Ram Krishnamurthy, John McAllister, Roger Woods, Tarek El-Ghazawi, 2011-03-15 This book constitutes the refereed proceedings of the 7th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2011 held in Belfast UK in March 2011 The 40 revised papers presented consisting of 24 full papers 14 poster papers and the abstracts of 2 plenary talks were carefully reviewed and selected from 88 submissions The topics covered are reconfigurable accelerators design tools reconfigurable processors applications device architecture methodology and simulation and system architecture

High-Performance Computing and Networking Bob Hertzberger, Alfons Hoekstra, Roy Williams, 2003-05-15

This book constitutes the refereed proceedings of the 9th International Conference on High Performance Computing and Networking HPCN Europe 2001 held in Amsterdam The Netherlands in June 2001 The 67 revised papers and 15 posters presented were carefully reviewed and selected from a total of almost 200 submissions Among the areas covered are Web grid applications of HPCN end user applications computational science computer science and Java in HPCN Parallel Computing Technologies Victor Malyskin,2007-08-29 This book constitutes the refereed proceedings of the 9th International Conference on Parallel Computing Technologies PaCT 2007 held in conjunction with the Russian Taiwan symposium on Methods and Tools of Parallel Programming of Multicomputers It covers models and languages applications techniques for parallel programming supporting cellular automata as well as methods and tools of parallel programming of multicomputers Constructive Methods for Parallel Programming Sergei Gorlatch,2002 Constructive Methods for Parallel Programming Proceedings ,2005 **Compiler and Runtime Techniques to Identify and Optimize Atomic Blocks in Parallel Programs** Stefan Kempf,2014-11-03 Atomic blocks are a programming language construct that marks sections of code that multiple threads may not execute concurrently The implementation of the mutual exclusion is left to the compiler or the runtime system To avoid race conditions however developers are still responsible to detect all places in the code where atomic blocks are needed It is of course also important that mutual exclusion is implemented efficiently This thesis therefore pursues two goals First we develop a data dependence analysis that identifies potential critical sections in a parallel program presents them to the programmer and then adds atomic blocks to the code Second we enhance existing techniques software transactional memory STM and lock inference to implement atomic blocks Our approach combines the advantages of STM and lock inference which allows atomic blocks to be implemented more efficiently The evaluation shows that our approach identifies atomic blocks that closely match those atomic blocks that an expert would add to the code Compared to a pure STM or lock inference implementation our combined approach to implement atomic blocks speeds up execution times by a factor between 1.1 and 6.3 **American Book Publishing Record** ,1997 **Subject Guide to Books in Print** ,1991 **International Books in Print** ,1997 *Forthcoming Books* Rose Army,1997 **Books In Print 2004-2005** Ed Bowker Staff,Staff Bowker, Ed,2004 **Verzeichnis lieferbarer Bücher** ,1999 German books in print ,1997 *Dataquest* ,1993 *Proceedings of the ASME Fluids Engineering Division Summer Meeting* American Society of Mechanical Engineers. Fluids Engineering Division. Summer Meeting,1996

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