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# **DATA MINING AND DIAGNOSING IC FAILS**

Leendert M. Hulsman

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 Springer

# Datamining And Diagnosing Integrated Circuit Falls

**Ting, I-Hsien,Hong, Tzung-Pei,Wang,  
Leon Shyue-Liang**

## **Datamining And Diagnosing Integrated Circuit Falls:**

*Data Mining and Diagnosing IC Falls* Leendert M. Huisman, 2006-10-03 This book grew out of an attempt to describe a variety of tools that were developed over a period of years in IBM to analyze Integrated Circuit fail data The selection presented in this book focuses on those tools that have a significant statistical or datamining component The danger of describing statistical analysis methods is the amount of non trivial mathematics that is involved and that tends to obscure the usually straightforward analysis ideas This book is therefore divided into two roughly equal parts The first part contains the description of the various analysis techniques and focuses on ideas and experimental results The second part contains all the mathematical details that are necessary to prove the validity of the analysis techniques the existence of solutions to the problems that those techniques engender and the correctness of several properties that were assumed in the first part Those who are interested only in using the analysis techniques themselves can skip the second part but that part is important if only to understand what is being done

Data Mining for Design and Manufacturing D. Braha, 2013-03-14 *Data Mining for Design and Manufacturing Methods and Applications* is the first book that brings together research and applications for data mining within design and manufacturing The aim of the book is 1 to clarify the integration of data mining in engineering design and manufacturing 2 to present a wide range of domains to which data mining can be applied 3 to demonstrate the essential need for symbiotic collaboration of expertise in design and manufacturing data mining and information technology and 4 to illustrate how to overcome central problems in design and manufacturing environments The book also presents formal tools required to extract valuable information from design and manufacturing data and facilitates interdisciplinary problem solving for enhanced decision making Audience The book is aimed at both academic and practising audiences It can serve as a reference or textbook for senior or graduate level students in Engineering Computer and Management Sciences who are interested in data mining technologies The book will be useful for practitioners interested in utilizing data mining techniques in design and manufacturing as well as for computer software developers engaged in developing data mining tools

*Official Gazette of the United States Patent and Trademark Office*, 2001 **Microelectronic Test Structures for CMOS Technology** Manjul Bhushan, Mark B. Ketchen, 2011-08-26 *Microelectronic Test Structures for CMOS Technology and Products* addresses the basic concepts of the design of test structures for incorporation within test vehicles scribe lines and CMOS products The role of test structures in the development and monitoring of CMOS technologies and products has become ever more important with the increased cost and complexity of development and manufacturing In this timely volume IBM scientists Manjul Bhushan and Mark Ketchen emphasize high speed characterization techniques for digital CMOS circuit applications and bridging between circuit performance and characteristics of MOSFETs and other circuit elements Detailed examples are presented throughout many of which are equally applicable to other microelectronic technologies as well The authors overarching goal is to provide students and technology practitioners alike a practical guide to the disciplined design

and use of test structures that give unambiguous information on the parametrics and performance of digital CMOS technology

*Social Network Mining, Analysis, and Research Trends: Techniques and Applications* Ting, I-Hsien, Hong, Tzung-Pei, Wang, Leon Shyue-Liang, 2011-12-31 This book covers current research trends in the area of social networks analysis and mining sharing research from experts in the social network analysis and mining communities as well as practitioners from social science business and computer science Provided by publisher

*Dissertation Abstracts International* ,2007

**Directory of Published Proceedings** ,2002

**Index Medicus** ,2001

*Cumulated Index Medicus* ,1999

*The British National Bibliography* Arthur James Wells,2000

*Thomas Register of American Manufacturers and Thomas Register Catalog File* ,2002 Vols for 1970 71 includes manufacturers catalogs

**Thomas Register of American Manufacturers** ,2002 This basic source for identification of U S manufacturers is arranged by product in a large multi volume set Includes Products services Company profiles and Catalog file

**Byte** ,1998

**Library & Information Science Abstracts** ,2003

**Failure Analysis of Integrated Circuits** Lawrence C. Wagner,2012-12-06

Failure Analysis of Integrated Circuits Tools and Techniques provides a basic understanding of how the most commonly used tools and techniques in silicon based semiconductors are applied to understanding the root cause of electrical failures in integrated circuits These include applications specific to performing failure analysis such as decapsulation deprocessing and fail site isolation as well as physical and chemical analysis tools and techniques The coverage is qualitative and it provides a general understanding for making intelligent tool choices Also included is coverage of the shortcomings limitations and strengths of each technique Failure Analysis of Integrated Circuits Tools and Techniques is a must have reference work for semiconductor professionals and researchers

**Test and Diagnosis for Small-Delay Defects** Mohammad Tehranipoor, Ke Peng, Krishnendu Chakrabarty, 2011-09-08 This book will introduce new techniques for detecting and diagnosing small delay defects in integrated circuits Although this sort of timing defect is commonly found in integrated circuits manufactured with nanometer technology this will be the first book to introduce effective and scalable methodologies for screening and diagnosing small delay defects including important parameters such as process variations crosstalk and power supply noise

Automated Diagnosis of Path Delay Faults in Digital Integrated Circuits Pankaj Pant, 2000

**A Comprehensive Diagnosis Methodology for Characterizing Logic-behavior of Integrated Circuit Failures** Rao H. Desineni, 2006

*Statistical Diagnosis of IC Process Faults* John Kachig Kibarian, 1990 Abstract Manufacturing of integrated circuits involves the correct implementation of hundreds of processing steps Random variations in the processing conditions of these steps can result in integrated circuits which do not meet the design specifications These variations are usually the result of parametric or catastrophic faults in the manufacturing In competitive markets it is imperative for the engineers to learn about the mechanism for variability in the manufacturing process as soon as possible A critical part of this analysis entails the analysis of the intra wafer variability of the circuit performances

Machine Learning Support for Fault

Diagnosis of System-on-Chip Patrick Girard, Shawn Blanton, Li-C. Wang, 2023-03-13 This book provides a state of the art guide to Machine Learning ML based techniques that have been shown to be highly efficient for diagnosis of failures in electronic circuits and systems The methods discussed can be used for volume diagnosis after manufacturing or for diagnosis of customer returns Readers will be enabled to deal with huge amount of insightful test data that cannot be exploited otherwise in an efficient timely manner After some background on fault diagnosis and machine learning the authors explain and apply optimized techniques from the ML domain to solve the fault diagnosis problem in the realm of electronic system design and manufacturing These techniques can be used for failure isolation in logic or analog circuits board level fault diagnosis or even wafer level failure cluster identification Evaluation metrics as well as industrial case studies are used to emphasize the usefulness and benefits of using ML based diagnosis techniques

## **Datamining And Diagnosing Integrated Circuit Falls** Book Review: Unveiling the Power of Words

In a world driven by information and connectivity, the ability of words has become more evident than ever. They have the capability to inspire, provoke, and ignite change. Such could be the essence of the book **Datamining And Diagnosing Integrated Circuit Falls**, a literary masterpiece that delves deep in to the significance of words and their affect our lives. Published by a renowned author, this captivating work takes readers on a transformative journey, unraveling the secrets and potential behind every word. In this review, we shall explore the book is key themes, examine its writing style, and analyze its overall effect on readers.

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