

# **A Circuit-Level Implementation of Fast, Energy-Efficient CMOS Comparators for High-Performance Microprocessors\***

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ICCD 2002 Kenneth L. Shepard, IEEE Computer Society, IEEE Circuits and Systems Society, IEEE Solid-State Circuits Society, *On-Chip Communication Architectures* Sudeep Pasricha, Nikil Dutt, 2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design *On Chip Communication Architectures* is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication architectures explaining key concepts surveying research efforts and predicting future trends Detailed analysis of all popular standards for on chip communication architectures Comprehensive survey of all research on communication architectures covering a wide range of topics relevant to this area spanning the past several years and up to date with the most current research efforts Future trends that will have a significant impact on research and design of communication architectures over the next several years

**Performance Evaluation and Benchmarking** Lizy Kurian John, Lieven Eeckhout, 2018-10-03 Computer and microprocessor architectures are advancing at an astounding pace However increasing demands on performance coupled with a wide variety of specialized operating environments act to slow this pace by complicating the performance evaluation process Carefully balancing efficiency and accuracy is key to avoid slowdowns and such a balance can be achieved with an in depth understanding of the available evaluation methodologies *Performance Evaluation and Benchmarking* outlines a variety of evaluation methods and benchmark suites considering their strengths weaknesses and when each is appropriate to use Following a general overview of important performance analysis techniques the book surveys contemporary benchmark suites for specific areas such as Java embedded systems CPUs and Web servers Subsequent chapters explain how to choose appropriate averages for reporting metrics and provide a detailed treatment of statistical methods including a summary of statistics how to apply statistical sampling for simulation how to apply SimPoint and a comprehensive overview of statistical simulation The discussion then turns to benchmark subsetting methodologies and the fundamentals of analytical modeling including queuing models and Petri nets Three chapters devoted to hardware performance counters conclude the book Supplying abundant illustrations examples and case studies *Performance Evaluation and Benchmarking* offers a firm foundation in evaluation methods along with up to date techniques that are

necessary to develop next generation architectures      Handbook of Sensor Networks Ivan Stojmenovic,2005-09-19 The State Of The Art Of Sensor Networks Written by an international team of recognized experts in sensor networks from prestigious organizations such as Motorola Fujitsu the Massachusetts Institute of Technology Cornell University and the University of Illinois Handbook of Sensor Networks Algorithms and Architectures tackles important challenges and presents the latest trends and innovations in this growing field Striking a balance between theoretical and practical coverage this comprehensive reference explores a myriad of possible architectures for future commercial social and educational applications and offers insightful information and analyses of critical issues including Sensor training and security Embedded operating systems Signal processing and medium access Target location tracking and sensor localization Broadcasting routing and sensor area coverage Topology construction and maintenance Data centric protocols and data gathering Time synchronization and calibration Energy scavenging and power sources With exercises throughout students researchers and professionals in computer science electrical engineering and telecommunications will find this an essential read to bring themselves up to date on the key challenges affecting the sensors industry      **Advances in Computers** Marvin Zelkowitz,2004-05-26 Advances in Computers covers new developments in computer technology Most chapters present an overview of a current subfield within computer science with many citations and often include new developments in the field by the authors of the individual chapters Topics include hardware software theoretical underpinnings of computing and novel applications of computers This current volume emphasizes architectural issues in the design of new hardware and software system An architectural design evaluation process is described that allows developers to make sure that their source programs adhere to the architectural design of the specifications This greatly aids in the maintenance of the system Telecommunications issues are covered from the impact of new technology to security of wireless systems Quantum computing an exciting development that may greatly increase the speed of present computers is described The book series is a valuable addition to university courses that emphasize the topics under discussion in that particular volume as well as belonging on the bookshelf of industrial practitioners who need to implement many of the technologies that are described In depth surveys and tutorials on new computer technology Well known authors and researchers in the field Extensive bibliographies with most chapters All chapters discuss aspects of architectural design of new hardware and software Quantum computing is an exciting new prospect for future machine design      **CODES 2002** ,2002      *Wireless Sensor Networks* C.S. Raghavendra,Krishna M. Sivalingam,Taieb Znati,2006-12-15 Wireless Sensor Networks presents a comprehensive and tightly organized compilation of chapters that surveys many of the exciting research developments taking place in this field Chapters are written by several of the leading researchers exclusively for this book Authors address many of the key challenges faced in the design analysis and deployment of wireless sensor networks Included is coverage of low cost sensor devices equipped with wireless interfaces sensor network protocols for large scale sensor networks data storage

and compression techniques security architectures and mechanisms and many practical applications that relate to use in environmental military medical industrial and home networks **Index of Conference Proceedings** British Library. Document Supply Centre,2002 **IEEE Circuits & Devices** ,2002 *Advanced Techniques in Logic Synthesis, Optimizations and Applications* Kanupriya Gulati,2010-11-25 This book covers recent advances in the field of logic synthesis and design including Boolean Matching Logic Decomposition Boolean satisfiability Advanced Synthesis Techniques and Applications of Logic Design All of these topics are valuable to CAD engineers working in Logic Design Logic Optimization and Verification Engineers seeking opportunities for optimizing VLSI integrated circuits will find this book as an invaluable reference since there is no existing book that covers this material in a systematic fashion **Proceedings** ,2004

**Conference Proceedings** ,2003 Statistical Timing Analysis for Digital Circuit Design Lizheng Zhang,2005 **31st Annual International Symposium on Computer Architecture** ,2004 **Applied Cryptography and Network Security** ,2003 **Bio-Inspired Fault-Tolerant Algorithms for Network-on-Chip** Muhammad Athar Javed Sethi,2020-03-17 Network on Chip NoC addresses the communication requirement of different nodes on System on Chip The bio inspired algorithms improve the bandwidth utilization maximize the throughput and reduce the end to end latency and inter flit arrival time This book exclusively presents in depth information regarding bio inspired algorithms solving real world problems focussing on fault tolerant algorithms inspired by the biological brain and implemented on NoC It further documents the bio inspired algorithms in general and more specifically in the design of NoC It gives an exhaustive review and analysis of the NoC architectures developed during the last decade according to various parameters Key Features Covers bio inspired solutions pertaining to Network on Chip NoC design solving real world examples Includes bio inspired NoC fault tolerant algorithms with detail coding examples Lists fault tolerant algorithms with detailed examples Reviews basic concepts of NoC Discusses NoC architectures developed to date **Integrated Circuit and System Design** ,2003

Proceedings of the 2004 International Symposium on Performance Evaluation of Computer and Telecommunication Systems Mohammad Salameh Obaidat,Abbas Jamalipour,Franco Davoli,2004 Issues for 2011 cataloged as a serial in LC **CASES 2004** Mary Jane Irwin,2004 Synthesis from Software Binaries Gregory Michael Stitt,2007

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